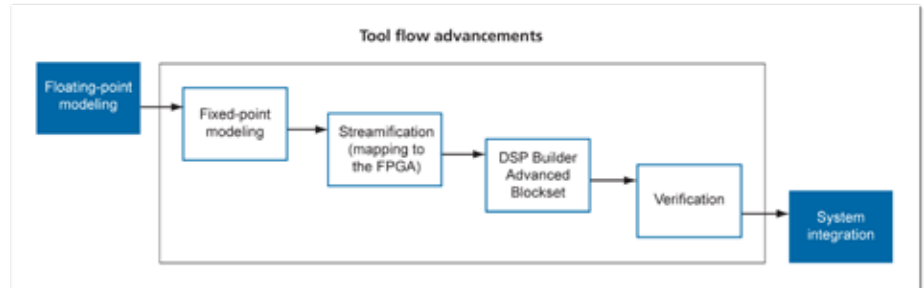


Military, Aerospace, and Defense DSP Productivity Seminars

Presented by Altera and The MathWorks

Want to enhance your DSP design productivity?



Schedule/locations

Date: Starting in April 2009

Location: Your facility

Interested? Contact your local field sales engineer or field application engineer for more information.

Schedule a seminar and learn about:

- Altera's long-range DSP technical capabilities roadmap and military customer support plan
- The MathWorks MATLAB and Simulink tools for Model-Based Design and verification
- Modeling and simulating systems and algorithms at a high level of abstraction
- Designing algorithms for implementation on embedded hardware and software
- Verifying algorithms and systems early in the design, and re-using verified algorithms throughout the development process
- Altera's end-to-end tool flow for DSP modeling and design

Other agenda topics

Our DSP technical specialists will be available to take your military design questions. You'll learn how incremental compilation, team-based design, and other resources can save time and money, and you will gain an understanding of how 40-nm FPGAs and ASICs fit into a managed risk approach.

We'll highlight a sensor front-end design example using:

- Highly optimized code generation with Altera's synthesis tool and MathWorks MATLAB and Simulink
- 4 channels using 3 GSPS ADCs running at 350 MHz

www.altera.com/military

www.mathworks.com/aero