



EPF10K200E

Embedded Programmable Logic Device

May 1999, ver. 1

Errata Sheet

Preliminary Information

This errata sheet gives updated information for EPF10K200E devices in 672-pin FineLine BGA packages from the NAA440028, NAB440061, and NAB440079 lot codes. The lot code is marked on the top side of the device.

For devices in these packages, the No Connect (NC) ball AE11 is connected to VCCIO . Additionally, a number of NC pins are connected together internally; these pins should be left unconnected on the board. The following list shows the connected balls. Each pair of pins is connected; pairs of pins are not connected to other pins.

- A4 and B4
- A6 and B6
- A10 and B10
- A12 and B12
- A13 and B13
- A19 and B19
- A22 and B22
- AE6 and AF6
- AE7 and AF7
- AE8 and AF8
- AE9 and AF9
- AE10 and AF10
- AE12 and AF12

EPF10K200EFC672 devices with other lot codes do not have NC balls tied together or to VCCIO .



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