

Introduction

This document addresses known errata and documentation changes for version 1.3.1 of the POS-PHY Level 2 & 3 Compiler.

Errata are design functional defects or errors. Errata may cause the POS-PHY Level 2 & 3 Compiler to deviate from published specifications.

Documentation changes include typos, errors, unclear descriptions or omissions from current published specifications or product documents. These documentation changes or clarifications will be incorporated into an upcoming release of the POS-PHY Level 2 & 3 Compiler.

POS-PHY Level 2 & 3 Compiler v1.3.1 Issues

Altera has identified the following issue that affects the POS-PHY Level 2 & 3 Compiler v1.3.1:

1. [“The User Guide Incorrectly Describes the Global Interface Reset Signals”](#).

The User Guide Incorrectly Describes the Global Interface Reset Signals

The Global Interface section of the *POS-PHY Level 2 & 3 Compiler User Guide* describes three reset signals—`treset_n`, `rreset_n`, and `reset_n`—and one clock signal, `clk`.

The `treset_n` and `rreset_n` descriptions incorrectly specify that these resets can be deasserted synchronously to `clk`. In fact, `treset_n` is deasserted synchronously to `tfclk`, and `rreset_n` is deasserted synchronously to `rfclk`.

Affected Configurations

This problem affects all variations of the POS-PHY Level 2 and 3 MegaCore® functions.

Design Impact

This issue does not impact the MegaCore function.

Workaround

No workaround is available.

Solution Status

The `reset` signal descriptions in the *Global Interface Description* will be corrected in the next release of the *POS-PHY Level 2 & 3 Compiler User Guide*.

Contact Information

For more information, contact Altera's mySupport website at www.altera.com/mysupport and click **Create New Service Request**. Choose the **Product Related Request** form.

Revision History

Table 1 shows the revision history.

Version	Date	Details of Change
1.0	August 2005	First release of the POS-PHY Level 2 & 3 Compiler errata sheet for version 1.3.1.



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