

This document addresses known errata and documentation issues for the CRC Compiler version 7.1. Errata are functional defects or errors, which may cause the CRC Compiler to deviate from published specifications. Documentation issues include errors, unclear descriptions, or omissions from current published specifications or product documents.



For the most up-to-date errata for this release, continue to refer to the errata page on the Altera® website:

www.altera.com/literature/es/es_crc_71.pdf

CRC Compiler v7.1 Issues

This section describes the CRC Compiler version 7.1 issues.

Testbench Directory Generated When You Create a Simulation Model

When you create a simulation model, the CRC compiler automatically creates a **testbench** directory in the project directory for you. If you follow the **Running the Testbench Example** steps in the *CRC Compiler User Guide* to create the generator and checker files, another **testbench** directory is created as a subdirectory of the initial **testbench** directory resulting in the following directory structure:

```
c:\altera\projects\crc_project\testbench\testbench
```

when the initial directory is

```
c:\altera\projects\crc_project\testbench
```

Affected Configuration

All CRC MegaCore function variations are affected.

Design Impact

There is no design impact for this issue.

Workaround

The `testbench` subdirectory (`testbench\testbench`) of the initial `c:\altera\projects\crc_project\testbench` directory may be deleted.

Solution Status

Currently, there are no plans to change this behavior.

Contact Information

For more information, contact Altera's mySupport website at www.altera.com/mysupport and click **Create New Service Request**. Choose the **Product Related Request** form.

Revision History

The table below shows the revision history for the *CRC Compiler v7.1 Errata Sheet*.

Table 1. CRC Compiler Errata Sheet Revision History		
Version	Date	Errata Summary
1.0	May 2007	Testbench Directory Generated When You Create a Simulation Model



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