

This document addresses known errata and documentation issues for the POS-PHY Level 4 MegaCore® function version 6.1. Errata are functional defects or errors, which may cause the POS-PHY Level 4 MegaCore function to deviate from published specifications. Documentation issues include errors, unclear descriptions, or omissions from current published specifications or product documents.

Table 1 shows the issues that affect the POS-PHY Level 4 MegaCore function v6.1.

Table 1. POS-PHY Level 4 MegaCore Function v6.1 Issues	
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For the most up-to-date errata for this release, refer to the errata sheet on the Altera® website:

www.altera.com/literature/es/es_posphy4_61.pdf

POS-PHY Level 4 MegaCore Function v6.1 Issues

This section describes the POS-PHY Level 4 MegaCore function v6.1 issues.

Gate-Level Simulations Fail in Synopsis VCS

Gate-level simulations of the POS-PHY Level 4 MegaCore function fail in Synopsis VCS.

Affected Configurations

This issue affects all configurations.

Design Impact

There is no design impact.

Workaround

Before you simulate in VCS, in the Quartus II software follow these steps:

1. On the Assignments menu click **EDA Tool Settings**.
2. Expand the list and click **Simulation**.
3. In the Tool Name list, select **VCS**.
4. Click **More settings**.
5. Turn on **Generate netlist for functional simulation**.

Solution Status

This issue will be fixed in a future release of the POS-PHY Level 4 MegaCore function.

The Calendar Length Value Cannot Equal 256

If the transmitter's status interpretation mode is set to pessimistic, the programmable calendar length support parameter must be less than the maximum number of ports (< 256), unless the asymmetric port support parameter is enabled.

Affected Configurations

This issue affects all transmitter variations of the MegaCore function that use the pessimistic mode for status interpretation, and that do not have the asymmetric port support parameter enabled.

Design Impact

The status first-in first-out (FIFO) buffer may lock up. The scheduler in individual buffers variations of the MegaCore function may also lock up.

Workaround

If you turn on the programmable calendar length support in your variation, make sure that you set the calendar length value—via a pin or the Avalon® register—to less than the maximum calendar length (that is, 256); unless asymmetric port support is enabled, in which case you select the maximum calendar length in IP Toolbench.

Solution Status

This issue will be fixed in a future release of the POS-PHY Level 4 MegaCore function.

The Receiver Simulation Model Does Not Synchronize

Gate-level simulations of receiver variations may not synchronize because of X states.

Affected Configurations

This issue may affect all receiver variations that have the dynamic phase alignment (DPA) feature turned on, in Stratix® III, Stratix II GX, Stratix II, and Hardcopy® II devices.

Design Impact

The `stat_rd_dip4_oos` signal becomes X and the SPI-4.2 receiver status channel (`rstat`) sends continuous framing ('b11), thus data does not flow.

Workaround

There is no workaround.

Solution Status

This issue will be fixed in a future release of the POS-PHY Level 4 MegaCore function.

IP Toolbench Error After Changing the Device Family

If you change the device family when editing an existing custom megafunction variation (POS-PHY Level 4 MegaCore function variation) without first changing the device family in the Quartus II project, an error may occur when generating the MegaCore function. This results in a MegaCore function generation error message.

This issue also applies when creating a new custom megafunction variation, if you use a different device family to that specified in the Quartus II project.

Affected Configurations

This issue can affect all configurations.

Design Impact

You may not be able to generate a MegaCore function.

Workaround

Before using the MegaWizard Plug-In Manager to create or edit a POS-PHY Level 4 custom megafunction variation, make sure that a Quartus II project exists and that the required device family is set in the project. To set the device family, select **Device** from the **Assignments** menu in Quartus II.

When using the MegaWizard Plug-In Manager to create or edit the megafunction variation, set the device family to be the same as the device family set in the Quartus II project. You can set the device family in the **Basic Parameters** tab when parameterizing the MegaCore function.

Solution Status

This issue will be fixed in a future release of the POS-PHY Level 4 MegaCore function.

IP Toolbench Fails When Generating IP Functional Simulation Models for HardCopy Stratix Devices

If you select **HardCopy Stratix** in the MegaWizard® Plug-In Manager and you turn on **Generate Simulation Model** and Generate a MegaCore function variation, IP Toolbench fails with an error.

Affected Configurations

This issue affects all configurations.

Design Impact

You cannot generate an IP functional simulation model.

Workaround

Select the Stratix family in the MegaWizard Plug-In Manager.

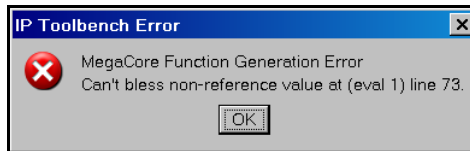
Solution Status

This issue will never be fixed.

IP Toolbench Generation Fails if the Generation Is Cancelled and Restarted

By clicking the IP Toolbench **Generate** button you start generating a POS-PHY Level 4 MegaCore function variation. If, during generation, you click the **Cancel** button (**Generation** window) and click the IP Toolbench **Generate** button again to restart the generation, IP Toolbench fails and produces the following error message:

Figure 1. IP Toolbench Generation Error Message



Affected Configurations

This issue affects all variations of the MegaCore function.

Design Impact

IP Toolbench does not generate any files.

Workaround

To cancel a generation and avoid this error, follow these steps:

1. Click the **Cancel** button in the **Generation** window.
2. Close IP Toolbench by clicking the **x** in the upper right corner.
3. Relaunch IP Toolbench from the MegaWizard Plug-In Manager (Tools menu).



Refer to the Getting Started chapter of the *POS-PHY Level 4 MegaCore Function User Guide* for instructions on using IP Toolbench.

Solution Status

This issue will be fixed in a future release of the POS-PHY Level 4 MegaCore function.

Contact Information

For more information, contact Altera's mySupport website at www.altera.com/mysupport and click **Create New Service Request**. Choose the **Product Related Request** form.

Revision History

Table 2 shows the revision history for the *POS-PHY Level 4 MegaCore Function v6.1 Errata Sheet*.

Version	Date	Errata Summary
1.1	March 2007	Added Gate-Level Simulations Fail in Synopsis VCS issue.
1.0	December 2006	First release.



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