

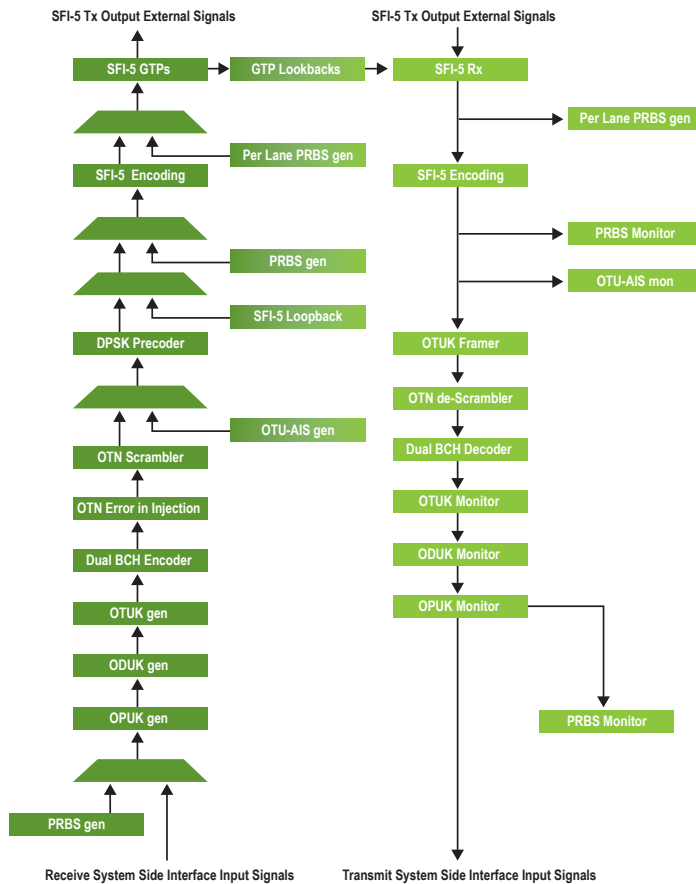
# Dual BCH FEC IP Cores

## General Features

- 8.39dB net coding gain at  $10^{-15}$  output BER
- 10G, 40G, or 56G rates, and variable rates in between
- Generic CPU interface for control and monitoring

## Application Diagrams

### Example Application: 40G



## Key Features

- IP core is smaller than BCH/RS EFEC with similar gain
- Supports for ASIC and FPGA implementations
- Ideal for 112G (2x56G) transport applications

## Statistics:

- corrected bits
- corrected ones
- corrected zeros
- corrected codes
- uncorrectable codes

## Related Products

For additional information regarding Altera FOTN IP solution visit [www.altera.com](http://www.altera.com).

## Want to Dig Deeper?

For more information about this IP cores, please contact your Altera® sales representative or FAE, or visit [www.altera.com](http://www.altera.com).

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