



## DSP Builder

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# Release Notes and Errata



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This chapter describes new features, revision history, known errata and documentation changes for the DSP Builder standard blockset.

## Revision History


Table 1–1 lists the revision history for the DSP Builder standard blockset.

**Table 1–1. DSP Builder Standard Blockset Revision History**

Version	Date	Revision
11.1	October 2011	<ul style="list-style-type: none"> <li>■ Support for 64-bit Windows OS</li> <li>■ SignalTap integration available on all supported platforms</li> </ul>
11.0	April 2011	Added Virtual Pins block.
10.1	December 2010	Maintenance release.

## Errata

Errata are functional defects or errors, which may cause DSP Builder to deviate from published specifications.

-  For the most recent list of errata for DSP Builder, refer to the [Knowledge Base](#) page of the Altera website. Also, you can use the Knowledge Base to search for errata based on the product version affected and other criteria.



This chapter describes new features, revision history, known errata and documentation changes for the DSP Builder advanced blockset.

### Product Revision History

Table 2–1 lists the product revision history for the DSP Builder advanced blockset.

**Table 2–1. DSP Builder Advanced Blockset Product Revision History**

Version	Date	Revision
11.1	October 2011	<ul style="list-style-type: none"> <li>■ Added support for Arria V devices and HardCopy compatibility mode</li> <li>■ Added faster system regeneration—subsystems only regenerated if differences are detected</li> <li>■ Improved quality of results:               <ul style="list-style-type: none"> <li>■ All Stratix V FIR benchmarks achieve 450 MHz</li> <li>■ 95% of Arria V FIR benchmarks achieve 310MHz</li> <li>■ 28-nm DSP block features inferred in subsystems</li> <li>■ Logic reduction and <math>f_{MAX}</math> improvement across most floating-point designs</li> </ul> </li> <li>■ Added external memory interfaces:               <ul style="list-style-type: none"> <li>■ Simulates DDR3 SDRAM controller</li> <li>■ Generates Avalon-MM master interfaces for Qsys</li> </ul> </li> <li>■ Improved loadable counter with run-time loading of initial value, modulo, and step-size</li> <li>■ Improved variable latency delay with simpler design flow for loop-based designs (especially floating-point designs)</li> <li>■ Enhanced testbench for floating-point designs               <ul style="list-style-type: none"> <li>■ Accepts verification scripts</li> <li>■ Allows holistic rather than sample-based comparisons</li> </ul> </li> <li>■ Added single and multichannel Cholesky solvers design examples</li> </ul>
11.0	April 2011	Maintenance release.
10.1	December 2010	<ul style="list-style-type: none"> <li>■ Added new blocks</li> <li>■ Added new design examples</li> </ul>

### Errata

Errata are functional defects or errors, which may cause DSP Builder to deviate from published specifications.



For the most recent list of errata for DSP Builder, refer to the [Knowledge Base](#) page of the Altera website. Also, you can use the Knowledge Base to search for errata based on the product version affected and other criteria.



This chapter provides additional information about the document and Altera.

## Document Revision History

The following table shows the revision history for this document.

Date	Version	Changes
1 November 2011	11.1	Updated for software version 11.1.
15 May 2011	11.0	Updated for software version 11.0.
15 December 2010	10.1	Updated for software version 10.1.

## How to Contact Altera

To locate the most up-to-date information about Altera products, refer to the following table.

Contact (1)	Contact Method	Address
Technical support	Website	<a href="http://www.altera.com/support">www.altera.com/support</a>
Technical training	Website	<a href="http://www.altera.com/training">www.altera.com/training</a>
	Email	<a href="mailto:custrain@altera.com">custrain@altera.com</a>
Product literature	Website	<a href="http://www.altera.com/literature">www.altera.com/literature</a>
Non-technical support (General) (Software Licensing)	Email	<a href="mailto:nacomp@altera.com">nacomp@altera.com</a>
	Email	<a href="mailto:authorization@altera.com">authorization@altera.com</a>









**Note to Table:**

(1) You can also contact your local Altera sales office or sales representative.

## Typographic Conventions

The following table shows the typographic conventions this document uses.

Visual Cue	Meaning
<b>Bold Type with Initial Capital Letters</b>	Indicate command names, dialog box titles, dialog box options, and other GUI labels. For example, <b>Save As</b> dialog box. For GUI elements, capitalization matches the GUI.
<b>bold type</b>	Indicates directory names, project names, disk drive names, file names, file name extensions, software utility names, and GUI labels. For example, <b>\qdesigns</b> directory, <b>D:</b> drive, and <b>chiptrip.gdf</b> file.
<i>Italic Type with Initial Capital Letters</i>	Indicate document titles. For example, <i>Stratix IV Design Guidelines</i> .
<i>italic type</i>	Indicates variables. For example, $n + 1$ . Variable names are enclosed in angle brackets (< >). For example, <file name> and <project name>.pof file.

Visual Cue	Meaning
Initial Capital Letters	Indicate keyboard keys and menu names. For example, the Delete key and the Options menu.
“Subheading Title”	Quotation marks indicate references to sections within a document and titles of Quartus II Help topics. For example, “Typographic Conventions.”
Courier type	Indicates signal, port, register, bit, block, and primitive names. For example, <code>data1</code> , <code>tdi</code> , and <code>input</code> . The suffix <code>n</code> denotes an active-low signal. For example, <code>resetn</code> . Indicates command line commands and anything that must be typed exactly as it appears. For example, <code>c:\qdesigns\tutorial\chiptrip.gdf</code> . Also indicates sections of an actual file, such as a Report File, references to parts of files (for example, the AHDL keyword <code>SUBDESIGN</code> ), and logic function names (for example, <code>TRI</code> ).
	An angled arrow instructs you to press the Enter key.
1., 2., 3., and a., b., c., and so on	Numbered steps indicate a list of items when the sequence of the items is important, such as the steps listed in a procedure.
	Bullets indicate a list of items when the sequence of the items is not important.
	The hand points to information that requires special attention.
	A question mark directs you to a software help system with related information.
	The feet direct you to another document or website with related information.
 CAUTION	A caution calls attention to a condition or possible situation that can damage or destroy the product or your work.
 WARNING	A warning calls attention to a condition or possible situation that can cause you injury.
	The envelope links to the <a href="#">Email Subscription Management Center</a> page of the Altera website, where you can sign up to receive update notifications for Altera documents.