

Featured Highlights:

- [New Release: DSP Builder 3.0 With Hardware in the Loop \(HIL\)](#)
- [Altera Ships World's Biggest FPGA Providing 322 GMACs for DSP](#)
- [Low-Cost Cyclone II DSP Development Kit Features TI Data Converters](#)
- [See Altera DSP Solutions at TI Developer Conference](#)

Software

[New Release: DSP Builder 3.0 With Hardware in the Loop \(HIL\)](#)

Speed up your system-level simulations and shorten your debugging time using the latest version of DSP Builder. This tool links Quartus® II software and The MathWorks' MATLAB/Simulink environment, allowing designers to directly create digital signal processing (DSP) hardware designs.

[Download Now: Edge Detection Reference Design](#) **Free**

This reference design demonstrates the use of DSP Builder for modeling and developing video and image processing designs. The design is a complete system that uses SOPC Builder to integrate peripherals and the Nios® II processor to control user I/O.

Devices

[Altera Ships World's Biggest FPGA Providing 322 GMACs for DSP](#)

The Stratix® II EP2S180 leads the industry by offering 82 percent more logic than any other available competing FPGAs. These devices offer up to 768 embedded multipliers at 420 MHz for high-performance DSP applications.

[New HardCopy II Structured ASICs Enable High-Volume DSP Applications](#)

Use Stratix II FPGAs to create and verify your DSP designs, then migrate seamlessly to a low-cost, pin-compatible HardCopy® II device. Altera's latest generation structured ASIC features up to 2.2 million ASIC gates for system logic and additional 1.4 million dedicated gates for DSP blocks with over 8 Mbits of embedded memory.

Intellectual Property Cores & Development Kits

[Low-Cost Cyclone II DSP Development Kit Features TI Data Converters](#)

Altera's latest DSP development kit features Altera®Cyclone™ II FPGAs and Texas Instruments ADS5520 analog-to-digital (A/D) converters. The comprehensive platform allows engineers to design, prototype, and debug

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DSP applications for wireless infrastructure, medical diagnostic, imaging, and test and measurement equipment. The kit will ship in the second quarter of 2005.

[Partner Highlight: Astec IPSP for Video & Image Processing](#)

Astec IPSP is a high-performance prototyping platform for video and image processing. The system features a DDR DIMM for frame buffering, two independent 64-bit PCI expansion slots and supports multiple video output formats. The IPSP ships with a complete software support package.

Events, Net Seminars & Training

[See Altera DSP Solutions at TI Developer Conference](#)

Visit the TI Developer Conference February 15 to 17 and see how Texas Instruments and Altera are working together to provide DSP solutions for a broad range of applications. The conference has more than 100 technical sessions, including hands-on workshops, in signal-processing technologies.

[View Net Seminar: DSP Processing Using Altera FPGAs](#) **Free**

Track 1 of the SOPC World Net Seminar Series focuses on the use of FPGAs for DSP system implementation. Learn how FPGAs address the DSP performance GAP, and how design tools, IP, and development kits simplify DSP implementation on FPGAs.

[Altera DSP Papers and Classes Presented at Embedded Systems Conference](#)

Altera will be presenting DSP papers and classes in Booth 602 at the Embedded Systems Conference in San Francisco, March 6 -10. The conference features leading companies showcasing cutting-edge hardware, software, tools, and the full spectrum of system components.

[Updated Training Classes: Learn About the Latest Altera DSP Solutions](#)

Altera has updated the DSP Design Series of technical training courses. Learn how to use Altera's comprehensive design software environment, DSP-related IP cores, and performance-optimized devices to help jump-start the DSP design cycle.

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