

Please add announcements@altera.com to your address book to ensure that you receive future issues of the Code:DSP e-Newsletter.

ALTERA.

Code: DSP e-Newsletter

Featured Highlights:

- [Join The MathWorks, Barco & Altera at the Code:DSP Net Seminar Series--September 14, 21, 28](#)
- [Now Shipping! DSP Dev Kit With Industry's Highest Density FPGA](#)
- [Download the High-Performance TI DSP EMIF Bridge Core Reference Design](#)
- [Visit Altera at GSPx Global Signal Processing Expo and Conference](#)

[Take Our Reader Survey- -Enter to Win One of Ten \\$50 Amazon.com Gift Certificates](#)

Just answer six quick questions about your involvement in digital signal processing (DSP) system design and you will be entered to win one of ten \$50 Amazon.com gift certificates. Understanding your role will help us improve this newsletter. Take the survey by Friday, September 16 to be eligible for the drawing.

Software

[Download the High-Performance TI DSP EMIF Bridge Core Reference Design](#) **Free**

The high-performance EMIF bridge core reference design provides a connection between an Altera® FPGA and the external memory interface (EMIF) of Texas Instruments (TI) TMS320C64x family of digital signal processors. This reference design is available for both Stratix® II and Cyclone™ II devices. [Download Reference Design](#)

Intellectual Property Cores & Development Kits

[Now Shipping! DSP Dev Kit With Industry's Highest Density FPGA](#)

Do you need maximum DSP performance? Use this comprehensive development platform to design, prototype, and debug DSP computationally intensive applications on the industry's highest density FPGA. Put your orders in today!

[See the Latest Listing of Altera & Partner 3rd Party DSP Development Kits](#)

Are you looking for the ideal FPGA-based DSP development kit? You'll want to

Q3, 2005



visit the Altera DSP Development Kit page which provides an up to date listing of Altera and 3rd party development platforms. Solutions cover a range of cost and features as well as Altera product families. New kits include:

- [**GiDEL Ships PROCSpark II Development Board Featuring Altera's Low-Cost FPGAs**](#)

Featuring a Cyclone II EP2C35 device, the PROCSpark II enables the user to build complex designs at fair price. The cost and performance of the platform make it very attractive for production requirements.

- [**Altera and Mango DSP Announce Bluejay Video Processing Board Featuring Stratix II FPGAs**](#)

The Bluejay is a long cPCI 6U video and imaging board dedicated to applications requiring the highest processing power powered by four Altera Stratix II FPGAs. The card is ideal for the most demanding DSP applications such as high definition video compression, transcoding, military, imaging and communications algorithms.

[Back to top ▲](#)

Events, Net Seminars & Training

[**Join The MathWorks, Barco & Altera at the Code:DSP Net Seminar Series**](#)

Free

September 14, 21, 28, online - Join The MathWorks, Barco Silex and Altera at this three-part DSP net seminar series to hear from industry experts and see leading-edge products. Learn how the next-generation of FPGA solutions will enable you to develop architectures that boost DSP performance and lower overall costs.

[**Visit Altera at GSPx Global Signal Processing Expo and Conference**](#)

October 24 - 27, Santa Clara, CA, Booth 213 - Altera is proud to be a Silver Sponsor of GSPx 2005, the international conference for embedded signal processing applications. Come see Altera's latest solutions for video and image processing and high-speed digital communications. This conference explores the latest in embedded software, embedded hardware, EDA, and tools.

[**See Demonstrations of Altera's Video Solutions at IBC 2005**](#)

September 9 - 13, Amsterdam, Hall 10, Stand 20 - Visit Altera and its partners at the International Broadcasting Conference (IBC), to see demonstrations of integrated, cost-effective, performance-intensive video solutions implemented with Altera FPGAs.

[**Attend Altera's Training Module at Texas Instruments DSP Training**](#)

[**Europe 2005: *Implementing Systems with DSPs and FPGAs***](#)

September 12 - October 26, Several cities - Learn how to take advantage of new DSP and FPGA products and proven methods to make your system design process simpler and faster. All real-time embedded designs including video/imaging, audio, communications, biometrics, medical, automotive and

military must address common considerations to achieve the right mix of performance, power consumption, and system cost.

[Back to top ▲](#)

Support & Literature

[Visit Altera's Updated & Expanded Code:DSP Solutions Center](#)

The Altera DSP Solutions Center has comprehensive, up-to-date information and resources for DSP designers who want to reap the benefits of programmable logic.

[Find All of Altera's Latest DSP Product Documentation Quickly](#)

Do you need to know more about Altera DSP solutions? All the latest information on Altera's DSP products, including application notes, DSP Builder, IP Cores, development kits, and reference designs, are in one location.

[Learn How Altera is Making DSP News](#)

Altera is making DSP headlines. For the latest news, visit this page to find all the DSP-related articles, conference papers, and press releases. Learn more about how Altera is making an important impact on the DSP industry.

[Back to top ▲](#)

As a subscriber to the Code:DSP e-Newsletter, you will receive a quarterly e-mail newsletter. Altera respects your privacy. If you no longer wish to receive this e-Newsletter, simply [unsubscribe](#).

[Subscribe](#) to additional Altera e-mail updates and e-Newsletters, or view/edit all of your Altera e-mail subscriptions.

Copyright© 1995-2005 Altera Corporation, 101 Innovation Drive, San Jose, California 95134, USA