

ALTERA.

Code: **DSP** e-Newsletter

Featured Highlights:

- [Now Available: DSP Builder v5.1 With New Simulation Capabilities](#)
- [Introducing Stratix II GX FPGAs With Embedded Transceivers for High-Speed DSP Applications](#)
- [Join The MathWorks & Altera Webinar on Wireless System Design](#)
- [Visit Altera at SDR Forum 2005](#)

Quarterly Spotlight

[Barco Makes Broadcast and Distribution Monitoring Over IP a Reality With Altera Programmable Solutions](#)

Altera® FPGAs and embedded processors are delivering unprecedented video processing and networking integration for Barco's Networked Broadcast Monitoring System. Barco's choice of Altera's programmable solutions allows them to integrate more functionality into a single chip, realize lower product costs and, as a result, extend their leadership in managed broadcast monitoring equipment.

Software

[Now Available: DSP Builder v5.1 with New Simulation Capabilities](#)

DSP Builder enables engineers to shorten the digital signal processing (DSP) design cycle by creating the hardware representation of a DSP design in an algorithm-friendly environment. The new version of DSP Builder allows the user to incorporate multiple HDL modules or Quartus® II software design projects directly into system models.

Devices

[Introducing Stratix II GX FPGAs With Embedded Transceivers for High-Speed DSP Applications](#)

Stratix® II GX FPGAs combine the industry's fastest and highest-density FPGA fabric with up to 20 low-power transceivers that operate between 622 Mbps to 6.375 Gbps to meet the requirements of high-speed DSP designs. The transceiver blocks provide complete support for a number of widely used protocols, including serial digital interface (SDI) and Serial RapidIO™.

Q4, 2005



Intellectual Property Cores & Development Kits

[Download FIR Compiler v3.3 With Enhanced Support for Multirate Filters](#)

The latest version of Finite Impulse Response (FIR) Compiler, Altera's powerful tool for generating customized digital filters on Altera devices, has been optimized for embedded multiplier usage in multirate decimation architectures. Also, FIR Compiler is now part of the Intellectual Property (IP) Base Suite and is included with a Quartus II software subscription.

[See the Latest Listing of Altera and Partner 3rd Party DSP Development Kits](#)

Are you looking for the ideal FPGA-based DSP development kit? You'll want to visit the Altera DSP Development Kit page which provides an up to date listing of Altera and 3rd party development platforms. Solutions cover a range of cost and features as well as Altera product families.

[IP Base Suite Now Available to Quartus II Subscribers](#) **Free**

To help you shorten your design time, Altera has bundled some of its most popular MegaCore® functions into the IP Base Suite, which is now included with a Quartus II software subscription.

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Events, Net Seminars & Training

[Join The MathWorks & Altera for the Wireless System Design Net Seminar](#) **Free**

November 17, online--This net seminar includes an overview of designing wireless systems with FPGA Hardware using Simulink and Altera's DSP Builder. Design examples will include Digital Upconversion, Crest Factor Reduction, and OFDM-Based Modulation. Register now!

[Visit Altera at SDR Forum 2005](#)

November 14-18, 2005, Orange Country--See Altera solutions focused on technology, standards, and business activity related to software defined radios (SDR) at the 2005 SDR Technical Conference and Product Exposition, Exhibitor Booth 211.

[View Now: "Code:DSP Net Seminar Series"](#) **Free**

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Join The MathWorks, Barco Silex and Altera in this three-part DSP net seminar series to hear from industry experts and see leading-edge products. Learn how the next-generation of FPGA solutions will enable you to boost DSP performance and lower overall costs.

DSP Resources

[Download DSP Reference Designs from Altera](#)

Are you looking for an example design to jumpstart your next high-performance DSP system? Check out the latest list of reference designs to see what's available--including the popular [high-performance EMIF bridge core reference design](#).

[Visit Altera's Updated Code:DSP Solutions Center](#)

The Altera DSP Solutions Center has comprehensive, up-to-date information and resources for DSP designers who want to reap the benefits of programmable logic.

[Find All of Altera's Latest DSP Product Documentation Quickly](#)

Do you need to know more about Altera DSP solutions? All the latest information on Altera's DSP products, including application notes, DSP Builder, IP Cores, development kits, and reference designs, are in one convenient location.

[Learn How Altera is Making DSP News](#)

Altera is making DSP headlines. For the latest news, visit this page to find all the DSP-related articles, conference papers, and press releases. Learn more about how Altera is making an important impact on the DSP industry.

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