

Quarterly Spotlight**[Webcast: Find Out About FPGA Design Using MATLAB and Simulink](#)**

Looking for ways to reduce your FPGA design cycle time? In this joint webcast with MathWorks, you will learn how using workflows based on MATLAB and Simulink can ease the FPGA design process and significantly reduce your design cycle time. [Watch the webcast](#) today!

FPGA Design with MATLAB and Simulink

▶ Watch the Webcast

[Webcast: Designing and Developing Pulse Doppler Radars Using FPGAs](#)

This joint webcast with MathWorks shows how you can simplify the development of radar systems using FPGAs with MATLAB and Phased Array System Toolbox. You'll also learn how to implement floating-point algorithms in FPGAs using the Altera® DSP Builder Advanced Blockset. [Watch the webcast](#) now.

Develop Pulse Doppler Radars Using FPGAs

▶ Watch the Webcast

[Implement FPGA Design with the OpenCL Standard](#)

Get higher performance, lower power, and faster time to market for your applications when you use the Open Computing Language (OpenCL) standard on an FPGA. [Watch the webcast](#) and [read the white paper](#) to find out more about Altera's OpenCL program for FPGAs.

OpenCL for FPGAs

▶ Learn More

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[From the DSP Wiki: Matrix Inversion Design Example](#)

The matrix inversion design example from the Altera DSP Wiki validates Altera's floating-point capability. [Download](#) the design example and see floating-point algorithm efficiency and performance in Stratix® IV GX and Arria® II GX devices.

Matrix Inversion Design Example

$$\begin{bmatrix} a & b & c \\ d & e & f \end{bmatrix} \begin{bmatrix} u & v \\ x & y \\ z & z \end{bmatrix} = \begin{bmatrix} au + bv + cx & av + bx + cz \\ du + ev + fx & dv + ex + fz \end{bmatrix}$$

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[Design Reference Papers: Implement Floating-Point DSP in FPGAs](#)

FPGAs are becoming the platform of choice for implementing floating-point digital signal processing (DSP). [Learn more](#) about using floating-point FPGAs for various DSP applications, such as radar and motor control, from these design reference papers.

[Training: Designing with DSP Builder Advanced Blockset](#)

Learn how to use the DSP Builder Advanced Blockset to implement your DSP solutions. [Take](#) the free online training for an overview, or [sign-up](#) for an in-depth, instructor-led or virtual class of the DSP Builder Advanced Blockset.

Sign Up for Embedded Newsletter**[Training: Overcome Challenges with the Video Design Framework](#)**

Find out how Altera's video design framework can help you overcome design challenges from this 8-hour instructor-led course. You'll learn to improve image format conversion quality, offload DSP processors, and reduce system cost and time to market. [Sign up](#) or request for a class today!

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