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Quarterly Spotlight

[Get Faster Software Build Times with New Nios II Software Build Tools for Eclipse](#)

Get the latest release of the Nios® II Embedded Design Suite, which includes the new Nios II Software Build Tools for Eclipse. These tools provide faster software build times, enhanced ease of use, and fine control over build options. [See what's new](#) in the latest release.



Embedded Software & Tools

[App Note: Experience Enhanced Real-Time Performance for the Nios II Processor \(PDF\)](#)

The latest release of the Nios II processor, v9.1, introduces the new Nios II Vectored Interrupt Controller (VIC), which reduces interrupt latency for fast, deterministic real-time applications. [Download](#) the VIC usage and applications application note today.

[Save With Nios II/e Core at No Charge](#) **Free**

Designed to use the fewest FPGA logic and memory resources of the Nios II processor family, the Nios II/e core is available at no charge with the latest release of Nios II Embedded Design Suite, v9.1. The Nios II/s and Nios II/f cores still require licensing. [Download the latest tools](#) to get the Nios II/e core.



[Evaluate FPGA-Based MCU with BeMicro Evaluation Board: \\$49](#)

If you are new to designing with FPGAs, or new to implementing microcontrollers in FPGAs, then the BeMicro FPGA-based MCU Evaluation Board is for you. This low-cost evaluation kit includes a complete lab to get you started designing a custom microprocessor system in an FPGA in just minutes. [Buy now!](#)



[Accelerate Your Design Cycle with Timesys Linux for the Nios II Processor](#)

Need a framework to get your Nios II embedded system up and running quickly with an open-source embedded Linux platform? Try LinuxLink from Timesys. LinuxLink provides an end-to-end platform design approach that works well with Altera® FPGAs. [Register for a free trial today!](#)



[Download Quartus II Web Edition v9.1 for Linux Beta](#)

The software you've been asking for is now here—Quartus® II Web Edition for Linux, in beta. This beta version offers limited feature and device support. See a list of [supported features](#), and [download](#) the software today.

Literature & Technical Resources

[Video Demo: 5 Easy Steps to Building an Embedded Processor System Inside an FPGA](#)

You may know about the cost and performance advantages of designing systems on a programmable chip. But did you know how easy it is to do? [Learn how by watching](#) this new 8-minute video.

[Video Demo: Learn How Dev Kit Utilities Shorten the Design Process](#)

Are you looking for a way to speed up the design process for your low-power FPGA applications? This video will give you a closer look at the Cyclone® III LS FPGA Development Kit, particularly two utilities that accelerate the design cycle. [Watch the video today!](#)

Altera also offers instructor-led training in locations around the US:

- [Designing with the Nios II Processor and SOPC Builder](#)
- [Developing Software for the Nios II Processor](#)

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