

Welcome to the July issue of the *Inside Edge*. This monthly e-Newsletter gives you the latest news about Altera's products and solutions.

Monthly Spotlight

[Introducing Cyclone II--The Lowest-Cost FPGAs Ever](#)

Offering 30% lower cost and more than three times the logic density than first-generation Cyclone™ devices, the new low-cost Cyclone II FPGA family features up to 68,416 logic elements, 1.1 Mbits of embedded memory, 150 18x18 multipliers, 4 PLLs, and support for multiple I/O standards.

FPGAs, CPLDs & Structured ASICs

[Stratix II Devices Now Shipping--First High-Density 90-nm FPGAs](#)

Altera continues to fulfill its commitment to customers for on-time delivery, as Stratix® II EP2S60 devices started shipping in June 2004. The 90-nm Stratix II devices offer more than double the logic density than current Stratix devices and are 50% faster than competitive devices.

[Cyclone Devices Support Extended Temperature Range Operation](#)

Cyclone devices support commercial (0°C to 85°C), industrial (-40°C to 100°C), and extended temperature range (-40°C to 125°C) applications. Get specific device support and other details for extended temperature range operation.

[Altera & Synopsys Collaborate on HardCopy Structured ASICs](#)

Driven by the rapidly growing demand for HardCopy® structured ASICs, Synopsys Professional Services now supports Altera's HardCopy design center. Designers can now use Synopsys' Galaxy Design Platform front-end flow to design their Stratix FPGAs and HardCopy Stratix devices.

Software, Intellectual Property & Development Kits

[Lower Your Development Costs with Quartus II Software Version 4.1](#) This release includes advanced support for Cyclone II devices and new optimization, verification, and ease-of-use features to lower development costs for FPGA, CPLD, and structured ASIC designs. All Cyclone II devices are now supported in Quartus® II Web Edition software.

[Download Analog Devices Link-Port Reference Design](#) **Free**

Download this Analog Devices Link-Port interface reference design connecting Stratix and Cyclone FPGAs to the Analog Devices TigerSharc series DSPs.

[AMPP Partner Aliathon Introduces New SONET/SDH IP Cores](#)

Altera's latest AMPPsm Partner, Aliathon, provides a full range of exceptionally compact and fast transmit and receive IP blocks for framing and mapping multi-channel payloads within PDH and SONET/SDH networks.

Technology & System Solutions

[Visit Altera's Updated Wireless System Solutions Web Site](#)

Get the latest details on Altera's solution offerings for the cellular infrastructure, broadband wireless, and software-defined radio markets including descriptions of advanced DSP

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techniques and reference designs from Altera.

Events, Training & Net Seminars

[Register for the Cyclone II Net Seminar](#) **Free**

Attend Altera's new net seminar: "ASIC Design Alternatives: Using New Low-Cost FPGAs for System Integration." Low-cost FPGAs, such as Altera's Cyclone II devices, offer the price, density, features, and performance to address a large part of the low- to mid-density ASIC market today.

[Learn to Create an Optimal Cyclone II Design](#)

Learn to create a high-performance and area-efficient Cyclone II design by attending the new course "Designing with Cyclone & Cyclone II Devices." Register for a class today!

Recent Articles about Altera

[FPGAs Pack a Cost-Conscious Counterpunch](#), **Electronic Design**

Learn how Altera's new Cyclone II device family delivers a full suite of benefits designed to continue their leadership in the low-cost FPGA market.

[Racing for the Gap](#), **FPGA Journal**

Learn how Altera leverages ASIC EDA powerhouse Synopsys for HardCopy structured ASICs.

[DAC: Altera Pushes HardCopy with Synopsys](#), **Electronic News**

Find out how the Synopsys/Altera partnership will provide access to additional physical design resources for HardCopy designs.

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