

Welcome to the June issue of the Inside Edge. This monthly e-Newsletter gives you the latest news about Altera's products and solutions.

Monthly Spotlight

[Introducing Nios II—the World's Most Versatile Processor](#)

Create a perfect-fit processor with three CPU cores, over 60 peripherals, and 256 user instructions available. Nios® II processors offer performance over 200 DMIPS, and logic for as little as 35 cents. Learn more about the world's most versatile processor.

FPGAs, CPLDs & Structured ASICs

[MAX II Devices Are Now Open for Orders](#)

Contact your Altera sales representative or distributor to place your order for the first MAX® II device, the EPM1270 CPLD. Follow this link to learn more about MAX II CPLDs.

[Stratix II FPGAs Are Now Open for Orders](#)

Contact your Altera sales representative or distributor to place your order for the first Stratix® II device, the EP2S60 FPGA. Find out more about the biggest and fastest FPGAs that can be ordered today.

Software, Intellectual Property & Development Kits

[Download Quartus II Web Edition Software](#) **Free**

The no-cost Quartus® II Web Edition software supports all MAX II CPLD and Cyclone™ FPGAs and includes everything needed to take designs from start to finish. Download Quartus II Web Edition software or request a Quartus II Software Starter Suite CD-ROM from the Altera® web site.

[Verify Transceiver Performance with the Stratix GX Development Kit](#) Users can test and demonstrate all Stratix GX transceiver features operating at speeds from 500 Mbps to 3.125 Gbps with Altera's High-Speed Development Kit, Stratix GX Edition. The development kit also supports dynamic phase alignment (DPA) on the source-synchronous I/O channels.

[Get Easy-to-Use Nios II Development Kits](#)

Nios II development kits, based on the Stratix and Cyclone device families, now feature Nios II processors. These kits contain everything needed to develop systems on a programmable chip, including CPUs, peripherals, robust tools, many examples, and complete documentation.

Technology & System Solutions

[New Altera DDR2, RLDRAM II, QDR II Hardware-Reference Platforms](#)

Altera has announced the industry's first FPGA-based hardware reference platforms for DDR2, QDR II, and RLDRAM II memories. The boards come with Gerber files, layout and termination recommendations, and signal-integrity analysis to simplify memory interface design effort.

[Learn about Video & Image Processing Technologies](#) **Free**

Attend this one-day complimentary seminar that will update and educate engineers, system architects, and project managers on video and image processing solutions. There will be

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emerging technology demonstrations and industry-expert presentations. (Locations: Boston, San Jose & Toronto)

Events, Training & Net Seminars

[Register for the Nios II Processor Net Seminar](#) **Free**

Register for the "Designing with Soft Processors for Optimal Performance, Features & Cost" net seminar. This net seminar will focus on how to easily create a custom embedded system-on-a-programmable-chip (SOPC) using the Nios II soft processor and SOPC Builder design tool.

[New Courses: Learn High-Speed & Embedded Design Skills](#)

Attend these courses to learn high-speed and embedded design techniques:

- High-Speed Design Using Stratix GX Devices
- Designing with Nios II & SOPC Builder

Register for a class today!

Recent Articles about Altera

[Packing Processing Power, FPGA Journal](#)

Learn about the advantages of using the Nios II soft embedded processor instead of a hard-wired processor. This article gives in-depth coverage of the Nios II family's new features and development tools.

[Altera Updates Nios processor, Electronics Weekly](#)

Read this comparison of three implementations of Nios II processors and an overview of the Nios II development kits and new software tools.

[Altera Rolls Next-Gen Nios Processor, EE Times](#)

Jordan Plofsky, senior vice president of Altera's applications business group, comments on Altera's strategy for soft processor cores in this review of the Nios II processor's features and improved development tools.

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