

ALTERA®

InsideEdge

Altera's Monthly e-Newsletter

Welcome to the September issue of the *Inside Edge*. This monthly e-Newsletter gives you the latest news about Altera's products and solutions.

Monthly Spotlight

[Now Shipping--DSP Dev Kit with Industry's Highest Density FPGA](#)

Do you need maximum digital signal processing (DSP) performance? Use this comprehensive development platform to design, prototype, and debug DSP computationally intensive applications on the industry's highest density FPGA, Stratix® II EP2S180.

FPGAs, CPLDs & Structured ASICs

[Use New Stratix II FPGA as a Prototype for HardCopy II HC220 Designs](#)

The EP2S130F780 Stratix II FPGA is now available. Develop your design in Quartus® II software as a HardCopy® prototype, then test the design in the FPGA. When the design is fully tested, Altera's HardCopy Design Center can migrate the design to a low cost HC220F780 structured ASIC.

[Run Your Own Density Benchmark Using Open Cores](#)

Do you question the results of vendor density benchmarks? Now you can run your own benchmark to compare density of Stratix II FPGAs to Virtex-4 using your own designs or publicly available open cores. Discover how you can fit more in Stratix II FPGAs!

[Discover Altera's Solutions for Better System Integration](#)

Get the benefits of integrating standard digital ICs and digital functions into programmable logic devices. Find out how to reduce your system costs, achieve higher performance, and increase design flexibility with Altera® FPGAs, CPLDs, and embedded processors.

Software, Intellectual Property & Development Kits

[See How Easy it is to Use Quartus II Software--Watch the Online Demos](#)

The Quartus II software is the only design environment available that supports FPGA, CPLD, and structured ASIC HardCopy device designs. The Quartus II online demos show how easy it is to get started using Quartus II software and highlight what's new in the latest version.

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Events, Training & Net Seminars

September 2005

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[View Today--First Session of the Code:DSP Net Seminar Series](#) **Free**

Online, September 14, 21, 28, 11:00 AM Pacific Time - Join The MathWorks, Barco Silex and Altera at this three-part DSP net seminar series to hear from industry experts and see leading-edge solutions. Learn how the next generation of FPGA products will enable you to boost DSP performance and lower overall costs.

[Learn to Use Incremental Compilation in Quartus II](#) **Free**

Online, anytime - Learn to use the incremental compilation flow in Quartus II to reduce compilation times while preserving the results of previous compilations. View this training anytime from the convenience of your desk.

[Learn Cutting-Edge Design Techniques--Attend SOPC/PLD World](#) **Free**

October & November, Asia, Japan and Europe - Attend SOPC World technical seminars to enhance your design abilities. This year's technical sessions and demos by Altera and partners include DSP, embedded design, jitter, power management, and design productivity.

[Attend Altera's High-Speed Seminar and DesignCon East 2005](#) **Free**

September 19 - 22, Worcester, MA - Attend Altera's High-Speed Seminar to learn about our high-speed serial I/O solutions. Visit Booth 407 at DesignCon East to see how pre-emphasis and equalization improves signal integrity at speeds up to 3.125 Gbps.

[See Altera at the Mentor Graphics European Solution Expo Seminars](#)

October and November, Europe - Visit Altera at the Mentor Graphics® European Solution Expo series to learn how Mentor Graphics can improve your FPGA, CPLD, structured ASIC, embedded system and DSP design efficiency from design entry through to PCB layout.

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Support & Literature

[Get Answers to Your ModelSim-Altera Software Questions](#)

Use the new one-stop ModelSim®-Altera software support page to help you verify your design and learn more about the ModelSim-Altera simulation software. This ModelSim-Altera resource web page offers a complete set of issue resolution tools, documentation and examples.

Altera in the News

[Altera, TI to Create Programmable PCI Express Design, EDN](#)

Learn how Altera and Texas Instruments (TI) are teaming up to create a standard-compliant PCI Express (PCIE) reference design using Altera's Cyclone™ II FPGAs and TI's TI X1 PHY (physical layer).

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