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InsideEdge

Altera's Monthly e-Newsletter

Welcome to the February 2006 issue of the *Inside Edge*. This monthly e-Newsletter gives you the latest news about Altera's products and solutions.

Monthly Spotlight

[Altera Now Offers 667-Mbps DDR2 SDRAM Solution for Stratix II FPGAs](#)

Stratix® II DDR2 SDRAM memory interface specification increased to 667 Mbps, a demonstration of the device family's excellent signal integrity. Learn more about this solution!

FPGAs, CPLDs & Structured ASICs

[Design Once for Both FPGA and Structured ASIC](#)

Discover how Altera makes it easy for you to prototype your design and start production early with Stratix II FPGAs. Then migrate seamlessly, without redesign, to volume production using HardCopy® II structured ASICs. Only Altera can offer these benefits, so why risk your next design on anything else?

[Get Over 100K Logic Elements in the Smallest Board Area](#)

Altera now offers the EP2S130 Stratix II FPGA (132K LEs, 6.4 Mbits memory) in the F780 package (29mm x 29mm). This is the smallest footprint you can get for a high-density FPGA with 100K+ logic elements.

Software, Intellectual Property & Development Kits

[New Software: Download Quartus II v5.1 Service Pack 1](#) **Free**

Quartus II v5.1 Service Pack 1 software is now available for v5.1 users. If you are not yet using Quartus® II v5.1, upgrade now. Version 5.1 introduces automated power optimization features and enhances incremental compilation technology. Upgrade to the performance and productivity leader today.

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Technology & End Markets

[UniTest Achieves Highest DDR2 SDRAM Speeds with Stratix II FPGAs](#)

UniTest needed the highest-performance DDR2 memory interfaces for their memory tester products. Using Stratix II FPGAs, they exceeded the 667-Mbps

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device specification and achieved up to 800 Mbps. See UniTest's Stratix II-enabled products and find out what they say about using Altera FPGAs.

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Events, Training & Net Seminars

[Attend an Altera High-Speed Design Roadshow Near You](#) **Free**

February & March - Altera is presenting the High-Speed Design Seminar in many locations worldwide. Learn the latest solutions for multi-gigabit serial interconnect design. Gain in-depth knowledge at this hands-on seminar.

[Visit Altera at the Texas Instruments Developer Conference 2006](#)

February 28 - March 2, Dallas, Texas, USA - Visit the TI Developer Conference and see how Texas Instruments and Altera are working together to provide DSP solutions for a broad range of applications. The conference has over 100 technical sessions in signal-processing technologies, including hands-on workshops.

[Meet Altera at DATE 2006--Design Automation & Test in Europe](#)

March 6 - 10, Munich, Germany - Experience the latest innovations in FPGA and design tools at DATE 2006 conference. Visit Altera at stand F34, to learn the latest about HardCopy structured ASICs, and FPGA solutions for ASIC prototyping and Quartus II software tool methodology.

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Support & Literature

[Online News & Views Investigates the Latest Technology Trends](#)

Evaluate how FPGAs and embedded transceivers ensure serial protocol signal integrity. Assess designing with FPGAs and structured ASICs. Examine the use of multi-processor solutions with FPGAs. For all this and more, read Online News & Views.

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Altera in the News



[Optimize Video Surveillance Systems with FPGA Coprocessors, DSP-FPGA.com](#)

Read this article to learn about the benefits of advanced video compression techniques such as MPEG-4, JPEG2000, and H.264 and their optimal implementation on a platform using a combination of digital signal processors and FPGA coprocessors.

[HardCopy II Structured ASIC Wins 2005 EDN China Award](#)

EDN China has announced Altera's HardCopy II structured ASICs has won the Leading Product Award in the EDN China Innovation Award 2005. Over 100 products from 43 companies competed to become the top products used for major innovation and adopted by engineers in China.

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