

Welcome to the July 2006 issue of the *Inside Edge*. This monthly e-Newsletter gives you the latest news about Altera's products and solutions.

July 2006

## Monthly Spotlight

### [Use MAX II CPLDs for Portable Designs: 50% Lower Cost & Power](#)

Altera's MAX® II CPLDs in ultra-small, form-factor packages offer portable system designers, on average, 50% lower cost and power than competing CPLDs. Get complete details, plus download the white paper, application notes, and power-down design.

## FPGAs, CPLDs & Structured ASICs

### [New Low-Density Cyclone II Devices Now Available](#)

Altera has introduced several new Cyclone™ II products, including the EP2C15, a 15K logic element (LE) device, giving designers a higher memory and multiplier-to-logic ratio. Also new are fast-on Cyclone II "A" FPGAs, ideal for automotive applications needing fast device wake up.

## Software, Intellectual Property & Development Kits

### [New DSP Builder 6.0 Supports The MathWorks' Latest Software](#)

Speed up your system-level simulations and shorten your debugging time using the latest version of DSP Builder. DSP Builder links Quartus® II software and The MathWorks MATLAB/Simulink software, allowing designers to use the same development environment for system and hardware design.

### [New Software: Download Quartus II V6.0 Service Pack 1](#)

Quartus II software version 6.0 service pack 1 is now available. This service pack adds full support for the new Cyclone II EP2C15A device. In addition, this release adds smaller ball-grid array (BGA) packages for the MAX II device family. Get Quartus II the #1 software in performance and productivity.

## Technology & End Markets

### [Download New Video Processing Reference Design](#) **Free**

Altera's new reference design shows how to speed development of a complete video system using Altera's intellectual property (IP) and tools. The design includes a BT.656 input, 1024x768 VGA output, scaler, deinterlacer, and color space converter, and runs on Altera's Video Development Kit, Cyclone II Edition.

## In This Issue:

[FPGAs, CPLDs & Structured ASICs](#)

[Software, Intellectual Property & Development Kits](#)

[Technology & End Markets](#)

[Events, Training & Net Seminars](#)

[Support & Literature](#)

[Altera in the News](#)

[Subscribe to Inside Edge](#)



### [Visit the Updated Altera Automotive Solutions Center](#)

Visit the recently revamped Altera® Automotive Solutions Center. The newly improved site has comprehensive automotive solutions such as graphic processing, car networking, and driver assistance. Learn more about Altera's premium solutions for the automotive market.

### [Cyclone II FPGAs at the Heart of Motorola's Award-Winning Canopy](#)

Motorola leverages the cost and performance advantages of Cyclone series FPGAs in their Canopy platform. The Canopy platform is part of the MOTOWi4 solutions portfolio for fixed, nomadic, and mobile networks, including metro WiFi and WiMAX applications. See why four generations of Motorola's Canopy have relied on Altera.

### [Read What Customers Are Saying About Stratix II GX FPGAs](#)

Read what companies like Panasonic, Motorola, and Ceterus Networks are saying about the robust noise immunity and excellent jitter performance of Stratix® II GX FPGAs, and why they use these transceiver-based FPGAs for applications like BTS baseband systems and switch fabrics.

## Events, Training & Net Seminars

### [Net Seminar Series: Optimizing High-Speed Serial Design](#) **Free**

Learn how to achieve successful high-speed serial system design with Stratix II GX FPGAs. This Altera net seminar series, in partnership with Cadence and Dr. Eric Bogatin, covers all aspects of transceiver design, from the challenge of board layout, to implementing key protocols like PCI Express.

### [Online Training: Nios II C2H Compiler Fundamental Now Available](#) **Free**

Learn to use the Nios® II C-to-Hardware (C2H) Compiler to create a hardware accelerator from a function in your Nios II C software code. Find out how to optimize your C code to help the Nios II C2H Compiler produce the most efficient accelerator possible. A full-day, instructor-led class is also available with hands-on labs.

### [See New Altera Solutions & Demonstrations at NIWeek August 8–10](#)

Austin, TX - Visit booth 1302 to see Altera demonstrations, including: Nios II support in National Instruments' LabView Embedded tool, PXI-Express with Cyclone II FPGAs, and a portable reference design platform.

## Support & Literature

### [Accelerate WiMAX Development with Altera's OFDMA Reference Design](#)

Scalable OFDMA is a key technology behind mobile WiMAX and future broadband wireless protocols. Learn how to use Cyclone II and Stratix II FPGAs for implementing advanced OFDMA symbol-level processing



