

April 2007 *Inside Edge*

Bringing you the latest Altera® product and solution news.

## Monthly Spotlight

### [Turn your ideas into revenue with Cyclone III FPGAs--Now shipping!](#)

Altera's new Cyclone® III FPGAs offer an unprecedented combination of low power, high functionality, and low cost to maximize your competitive edge. They're ideal for high-volume, cost-sensitive applications with prices starting as low as \$4. Order online today!

## FPGAs, CPLDs & Structured ASICs

### [New low-cost 65-nm FPGAs for power-sensitive applications](#)

Cyclone III FPGAs are the only FPGAs with up to 120K logic elements that consume less than 0.5 watts of static power. They help you manage thermal requirements, reduce or eliminate system cooling costs, and extend battery life for handheld applications.

### [Want to plug your board into any backplane slot? \(PDF\)](#)

Now you can! Each production Stratix® II GX FPGA includes an adaptive equalizer--a state machine that automatically sets the receive equalizer for the best transceiver eye opening, from 2.5 to 6.5 Gbps. Read more in this white paper, and request beta software from your local salesperson.

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## Software, Intellectual Property & Development Kits

### [Limited-time offer extended: ModelSim-Altera web edition](#) **Free**

Due to popular demand, this limited time offer has been extended! ModelSim®-Altera Web Edition now includes support for Cyclone III FPGAs, along with its support for both VHDL and Verilog HDL languages for behavioral and timing simulations. Download now!

### [Download new Quartus II software to design Cyclone III FPGAs](#) **Free**

Version 7.0 of Quartus® II Web and Subscription Edition software provides the fastest path to design completion for your low-cost Cyclone III FPGA designs. Cyclone III FPGA support includes TimeQuest timing analysis, SOPC Builder, PowerPlay power technology, and more.

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## Did You Know...

Why are Easter Eggs banned by some companies?

[Find out.](#)



[Click to enlarge](#)

### [Learning about Altera's embedded resources just got easier](#)

See how FPGAs add value to your embedded design. Visit the Embedded Processing Resource Center to find links to a comprehensive list of embedded products and support resources.

### [Monthly SI prescription with Dr. Bogatin: Weave and skew](#)

In real life, multi-gigabit serial link differential transmission pairs will experience intra-line skew. Intel initiated a Fiberwave work group which studies skews from various measurements and provides recommendations to mitigate it. Learn about signal integrity from the SI Doctor, Dr. Eric Bogatin.

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## Events, Training & Net Seminars

### [Training: Floorplan, analyze & perform ECOs with Chip Planner](#) **Free**

In this 1-hour online training, you'll learn to use three key Quartus II Chip Planner features: LogicLock for design floor planning, detailed chip analysis for timing closure, and the Change Manager to debug last-minute ECOs.

### [Net seminar: How fast are the fastest FPGAs? View now!](#) **Free**

Join this net seminar to learn how using Stratix III FPGAs makes meeting timing closure on high-performance digital designs easier. At least one speed grade faster than competing devices, making them the industry's fastest, Stratix III FPGAs are the only FPGAs with DDR3 support.

### [Instructor-led training: High-speed design using Stratix II GX FPGAs](#)

Chelmsford, MA and San Jose, CA - Learn to optimize your design by taking advantage of the Stratix II GX architecture, operating modes, and features. You'll get hands-on experience with its transceivers by compiling and analyzing designs using both hardware and software, with PCI Express as an example.

### [Improve your product's HD quality: Visit Altera at NAB2007](#)

April 16-19, Las Vegas, NV - As the broadcast industry embraces HD video, OEMs are challenged to deliver the best quality video. See how our HD quality solutions address your video challenges through collaboration with leading partners on affordable HD solutions.

### [Essential Principles of Signal Integrity Class by Dr. Eric Bogatin](#)

April 19-20, San Jose, CA - Register for the Principles of Signal Integrity class by the renowned SI evangelist, Dr. Eric Bogatin. This two-day lecture/workshop reveals how interconnects affect signal integrity, with live measurements and simulations. Space is limited, so register today!

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## Altera in the News

Our new Cyclone III FPGA family has the industry buzzing. See what two technology publications have to say about these FPGAs in new markets.

- [Low-power FPGAs eye DSP apps, EE Times](#)
- [Cyclone III stirs up new programmable-logic opportunities, Electronic News/EDN](#)

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### Most Popular News from Last Issue

#### [Try Cyclone II FPGAs with \\$150 development kit](#)

Check out the new, low-cost Cyclone II FPGA Starter Development Kit to try Cyclone II FPGAs. The RoHS-compliant kit includes an EP2C20 device on-board, Nios® II Embedded Design Suite, Quartus II software, reference designs, and demos. Order yours from Altera eStore today!

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