

March 2007 *Inside Edge*

Bringing you the latest Altera® product and solution news.

## Monthly Spotlight

### [New: Essential Principles of SI Class by Dr. Eric Bogatin](#)

April 19-20, San Jose, CA: Register for the Principles of Signal Integrity class by the renowned SI evangelist, Dr. Eric Bogatin. This two-day lecture/workshop reveals how interconnects affect signal integrity, with live measurements and simulations. Space is limited, so register today!

## FPGAs, CPLDs & Structured ASICs

### [Compare Stratix III DSP capabilities to discrete DSPs](#)

Stratix® III FPGAs deliver more DSP capabilities with less power, smaller board area, and lower costs than discrete DSPs. If your platform's DSP performance requirements change, Stratix III FPGAs are scalable across device densities within the same package.

[Back to Top](#)

## Software, Intellectual Property & Development Kits

### [Try Cyclone II FPGAs with \\$150 development kit](#)

Check out the new, low-cost Cyclone® II FPGA Starter Development Kit to try Cyclone II FPGAs. The RoHS-compliant kit includes an EP2C20 device on-board, Nios® II Embedded Design Suite, Quartus® II software, reference designs, and demos. Order yours from Altera eStore today!

### [Huge Discounts on PCI Express Solution: Only 2 weeks left!](#)

Only 2 weeks left for you get up to 69% off Altera's PCI Express IP. These industry-leading IP cores for x1, x4, and x8 endpoint applications are all discounted for a limited time. You can also receive a PCI Express Development Kit, Stratix II GX Edition, at no additional cost. Act now!

[Back to Top](#)

## Technology & End Markets

Huge Savings  
On Altera's Solutions



## On the Edge



[Click to enlarge](#)

See the first-ever comic devoted to the programmable logic industry.

## Inside Edge Poll

What's your favorite part of your job?

[Take the poll. See the results.](#)

## Did You Know...

...where Altera got its name?

[Find out.](#)

## [Turbocharge your FPGA design process white paper \(PDF\)](#)

SEmulation, a combination of HDL simulation and hardware emulation, can reduce development times, increase design security, and reduce costs. This white paper describes how Gleichmann Electronics' SEmulator works, along with examples of how it can apply to your designs.

## [Monthly Signal Integrity Prescription with Dr. Bogatin: 16 EMC Rules](#)

Maximize the chances of your design passing EMC certification by using the board-level design rules developed by the Electromagnetic Compatibility Lab at the University of Missouri at Rolla. Learn about signal integrity from Dr. Bogatin and submit your nagging SI questions to the SI Doctor.

[Back to Top](#)

## Events, Training & Net Seminars

### [Discover Altera's Complete Broadcast Solutions at NAB'07](#)

April 16-19, 2007, Las Vegas, NV. As the broadcast industry is embracing HD video, OEMs are challenged to deliver the best quality video. Find out how Altera addresses and resolves these challenges through leading partners' collaboration on affordable HD quality solutions.

### [Four new online training courses now available](#)

- [Constraining & Analyzing Timing for Source Synchronous Circuits](#)
- [Design Planning Guidelines for High-Density FPGAs](#)
- [I/O Management](#)
- [Signal Integrity Analysis with Third-Party Tools](#)

[Back to Top](#)

## Support & Literature

### [Application note: Remote configuration over Ethernet \(PDF\)](#)

Remotely update your product over Ethernet using a Nios II processor. You'll be able to prepare, update, and download your FPGA configuration to flash memory and trigger a reconfiguration remotely over Ethernet. Download the application note today.

### [New Parallel Flash Loader support](#)

Altera's new Parallel Flash Loader megafunction troubleshooter is always available to help you get answers to your PFL design questions. It quickly walks you through common implementation issues, giving you straightforward answers so you can get on with your design.

[Back to Top](#)

## Altera in the News

## [Designing an FPGA architecture for efficiency and high performance,](#)

### PLD DesignLine

How does an FPGA company decide on a particular architecture? This article walks you through the process used at Altera to develop the adaptive logic module (ALM). See how Altera's high-end FPGAs leverage the architecture to efficiently and cost-effectively meet timing budgets.

## [Dual view screen for cars uses Altera FPGA,](#) Electronics Weekly

With a dual-view screen, the driver can refer to the GPS while the passenger watches a movie. Sharp electronics chose Altera FPGAs to implement this innovative technology. Read more to see why.

[Back to Top](#)

### Most Popular News from Last Issue

#### [What should you consider when planning your design? \(PDF\)](#)

The new Design Planning chapter in the Quartus II Handbook provides tips on planning your design to take advantage of Quartus II features that help improve your productivity. Download it today!

---

To ensure that you receive future issues of Inside Edge, please add [announcements@altera.com](mailto:announcements@altera.com) to your address book.

As a subscriber to the Inside Edge e-Newsletter, you will receive a monthly email newsletter. Altera respects your privacy. If you no longer wish to receive this e-Newsletter, please [unsubscribe](#).

[Subscribe](#) to additional Altera e-mail updates and e-Newsletters, or view/edit all of your Altera e-mail subscriptions. Altera e-mail communications include:

- Product Announcements & Updates
- Code:DSP e-Newsletter
- Embedded e-Newsletter
- Net Seminar e-Newsletter