

May 2007 *Inside Edge*

Bringing you the latest Altera® product and solution news.

## Monthly Spotlight

### [QuickCast: Implement PCIe, GbE, and SRIO with low-cost FPGAs](#) **Free**

In just 15 minutes, learn the basics about Arria™ GX devices. You'll see how Arria GX FPGAs' signal integrity, support, and software translates to a low-risk design for you. You'll hit the ground running and get your design out the door fast when you choose Arria GX FPGAs.

## FPGAs, CPLDs & Structured ASICs

### [New low-cost Arria GX family optimized for PCIe, GbE, and SRIO](#)

Arria GX FPGAs with transceivers support PCIe, GbE, and SRIO protocols for a low-cost coprocessing option. Altera's signal integrity, support, and software expertise means that Arria GX devices are the lowest-risk choice. Learn more and design with no-cost software today.

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## Software, Intellectual Property & Development Kits

### [Now Shipping: \\$199 Cyclone III FPGA Starter Kit](#)

Start your next low-power design with the low-cost Cyclone® III FPGA Starter Kit. This kit is easy to use and includes everything you need to get started: development board with 25K LE FPGA, easy-to-use software, complete documentation, cables, and power supply. Order yours today!

### [Download Quartus II Web Edition software v7.1](#) **Free**

New Quartus® II Web Edition software v7.1 is now available for download. This latest version supports Arria GX FPGAs and introduces the new in-system sources and probes feature, improved TimeQuest performance, and an enhanced SOPC Builder GUI. Download today!

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## Technology & End Markets

Join the  
**Altera Forum**



Now in the eStore  
**Quartus II Software**  
▶ Buy Online

## Did You Know...

How spicy is the spiciest hot pepper?

[Find out.](#)



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## [SI Prescription with Dr. Bogatin: Embrace innovation to survive](#)

The 3 common business drivers for electronic companies are: to increase productivity, to reduce time to market, and to design competitive products. Innovation is essential to meeting these goals. Learn about new innovations and signal integrity from the SI Doctor, Dr. Eric Bogatin.

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### Events, Training & Net Seminars

## [Net Seminar: How to use Stratix III low-power technologies](#) **Free**

Learn how to take advantage of Stratix® III and Quartus II software features to achieve faster performance, higher integration, and lower power consumption. You'll learn to use Programmable Power Technology, Quartus II software PowerPlay power optimization, and other features.

## [Find out about Altera Automotive solutions at Design&Elektronik](#)

May 15, Ludwigsburg, Germany - Learn about Altera's solutions for embedded graphics, car networking, and flexible microcontroller development at this conference and exhibition.

## [New online training: Take advantage of Cyclone III capabilities](#) **Free**

Learn about the new low-cost and low-power technology solutions now available in Cyclone III FPGAs. You'll see what the latest tools and intellectual property (IP) provide for your designs. You'll also learn how Cyclone III features can solve your common design challenges.

## [Signal Integrity Characterization Techniques by Dr. Eric Bogatin](#)

June 4-5, San Jose, CA: Register for the Signal Integrity Characterization Techniques class by the renowned SI evangelist, Dr. Eric Bogatin. This two-day lecture/workshop shows you how to expertly using the TDR and VNA with a live demo and presentation. Space is limited, so register today!

## [Attend Structured ASIC Symposium to reduce design time & risk](#) **Free**

May 9 & 11: Baltimore, MD/Washington, DC and Raleigh, NC: Learn how companies use structured ASICs to get to market first, with exactly the right product their customers want. You'll also see the tools they use to prototype designs and test them with their customers.

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### Support & Literature

## [Share your ideas on the new Altera Forum](#) **Free**

Join the new Altera Forum, where the community of Altera users exchanges knowledge, projects, and ideas. It's a place for you to learn new techniques while sharing your own tips and tricks on using Altera devices, software, and IP. Register today to join the Altera Forum community.

## [Download now! Quartus II 7.1 Handbook for design implementation](#)

The 5-volume handbook guides you through the programmable logic design cycle from design to verification: Systems design with SOPC Builder, best design practices, TimeQuest for accurate timing, incremental compilation for productivity, power analysis, and in-system debug tools.

## [Interact with the FPGA using new In-System Sources & Probes \(PDF\)](#)

The new In-System Sources and Probes feature introduced in Quartus II software v7.1 allows you to use a JTAG connection to create virtual inputs into your design. Download the PDF to learn to create stimuli resulting in increased visibility in your debugging process.

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## Altera in the News

### [Nios II C-to-Hardware Compiler wins EDN Innovation Award](#)

The Nios® II C2H compiler, a productivity tool that increases the performance of embedded software, won a prestigious EDN Innovation Award in the software category. See how the Nios II C2H compiler improves the productivity and system performance of embedded computing platforms.

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