

November 2007 *Inside Edge*

Bringing you the latest Altera® product and solution news.

Monthly Spotlight

[Discover the Benefits of Using FPGAs in Industrial Applications](#)

With FPGAs, you can create a single, flexible industrial Ethernet, graphics control, or embedded processing design that supports all current standards and is protected from obsolescence. See how it's done:

- [Webcast: The Next Industrial Revolution: Low-Cost Cyclone® III FPGAs](#)
- [Webcast: Implementing Flexible Industrial Ethernet Solutions](#)
- [White Paper: A Flexible Solution for Industrial Ethernet](#)

FPGAs, CPLDs & Structured ASICs

[Webcast: Reduce Power & Thermal Dissipation with Low-Cost FPGAs](#)

For thermally challenged or space-constrained environments, choosing low-power devices can save you money—they can reduce or eliminate cooling system costs. See how to reduce power consumption in your designs in this 20-minute webcast.

[White Paper: Perform Accurate FPGA Benchmarks \(PDF\)](#)

Learn how to accurately benchmark FPGA performance and power and how to interpret industry benchmark results. This white paper is useful for any FPGA customer who is interested in comparing devices from different FPGA vendors when validating a design.

[Pricing Discount from Linear Technology for Altera Customers](#)

Register now to receive promotional pricing on Linear Technology devices used in Altera's Stratix®, Arria™ GX, Cyclone, and MAX® II development boards. You can get discounts on switching and linear regulators, controller circuits, and other devices for your next design.

[Reference Design: Protect FPGA Designs from Theft](#)

Protect your low-cost, high-volume FPGA design! Using the "identification, friend or foe" design security approach, you can keep your design secure even if the configuration data bitstream is captured.

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Software, Intellectual Property & Development Kits



See New Device Availability

[Arria GX](#)
[Cyclone III](#)
[Stratix III](#)



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[Upgrade to Quartus II Subscription Edition & Get DSP Builder](#) **Free**

Upgrade to Quartus® II Subscription Edition online and get DSP Builder at no cost. That's a \$3,995 value for only \$2,000! Quartus II Subscription Edition software offers up to 70% faster compilation times and 2X faster simulation over Web Edition. Offer expires Nov. 23. Upgrade today!

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Technology & End Markets

[Webcast: Drive Cost-Sensitive Automotive Applications with FPGAs](#)

Find out how low-cost FPGAs offer optimum solutions for a wide range of automotive infotainment, car networking, and driver assistance applications. Examples on how to reduce costs and gain flexibility in graphics processing and car networking applications are discussed.

[Build a 3GPP LTE Wireless Basestation](#)

Are you working on customized [channel card](#) and [RF card](#) designs? Learn how Altera's high-performance and scalable silicon, low-latency OFDMA-MIMO IP, productivity-enhancing tools, and ready-to-go development kits can help you reduce your design costs and get to market fast.

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Events, Training & Net Seminars

[New Training: High-Performance Memory Interfaces in FPGAs](#) **Free**

This new online training highlights the ease with which you can implement high-performance memory interfaces in an FPGA using Quartus II software. In 1.5 hours, you'll select, parameterize, and test memory controller IP and see how easy it is to implement memory interfaces on Altera FPGAs.

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Support & Literature

[New Resource Center for Incremental Compilation](#)

High-performance designs on high-density FPGAs require thorough design partition planning. See how the Quartus II incremental compilation feature can help you with incremental design, preserving performance on unchanged partitions and reducing overall compilation time.

[From the Forum: Choosing On-Board Termination with FPGA & DDR2](#)

Altera Forum user **kleckse** asked for some advice on termination for a direct DDR2 memory chip interface for his Cyclone III design. Read the comments and suggestions from other Forum users that helped answer his question, then post your own advice to gain Forum reputation today!

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Altera in the News

[Altera Bases 45-nm Success on TSMC Relationship](#), EE Times

Altera's next venture into a family of FPGAs that will be produced in the 45-nm process node is riding on the success of its earlier 65-nm successful relationship with foundry TSMC. Read more about what makes this strong partnership work so well.

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Most Popular News from Last Issue

[Build Your Cyclone III Design Right the First Time \(PDF\)](#)

Save time and avoid unnecessary headaches by using these insider tips on Cyclone III design. This new app note covers it all—from choosing the right device to the final test. The design checklist at the end summarizes everything in one place for easy reference.

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