

Monthly Spotlight

[Inspiration for Embedded Designs: Award-Winning White Papers](#)

Check out the top 19 white papers of the 2007 Nios® II Embedded Processor Design Contest, with finalists selected from over 700 entries. See how you can use Nios II processors in applications such as motion control, networking, and signal, data, and image processing.



FPGAs, CPLDs & ASICs

[OpenCore Designs Show Increased Stratix III Advantage vs Virtex-5](#)

See for yourself! Benchmarks on 7 large, popular OpenCore designs show that as design sizes increase, Stratix® III FPGAs are up to 65% faster, can fit an average 46% more logic, and compile 6x faster than Virtex-5. Learn more, download the design files, and run the benchmarks yourself!

Software, Intellectual Property & Development Kits

[Quartus II Software v7.2 SP2 Users: Update to SP3](#)

Quartus® II software version 7.2 service pack 2 users who are designing for Stratix III FPGAs need to update to SP3 for additional programming support. Download SP3 today!

[Four New Cyclone III FPGA-Based Development Kits Now Available](#)

If you are designing systems using DSP, video imaging, or USB 2.0 functional blocks, check out Altera's new Cyclone® III FPGA-based developments kits. Take advantage of the only low-power, low-cost 65-nm FPGA family with these new, ready-to-go kits.

[Develop HD Video Systems in Record Time](#)

Are you looking to develop HD video applications--fast--using the popular Cyclone III FPGA platform? Now you can get a Cyclone III video development kit from Altera's partner, Bitec, for \$1,895. This kit features an EP3C120F780 dev board with 2 Bitec video daughter cards. Order today!

Technology & End Markets

[Webcast: How Custom Logic Delivers Scalable Automotive Designs](#)

In this 15-minute webcast, learn how custom logic (FPGAs and HardCopy® ASICs) makes automotive design easier. You'll see how you can build graphics and audio controllers, using ready-made reference designs based on the industry's only automotive-grade 65-nm custom logic available today.

Events & Training

[Training: New System-on-Chip Designer Curriculum](#)

Need to embed a soft-core processor in your FPGA? Learn how with the

Webcast

Accelerating OFDMA-MIMO
Wireless System Design

[View Now](#)



See New Device Availability

[Arria GX](#)

[Cyclone III](#)

[MAX IIZ](#)

[Stratix III](#)

Inside Edge Poll

What do you think of our new newsletter design?

[Take the Poll. See the results.](#)



SOC Designer Curriculum. This course-by-course curriculum lists every class you need to become an expert SOC designer, including integrating processors and software on a Nios II Development Kit.

[Click to enlarge](#)

[ESC Session: Intelligent Power Management of FPGA Designs](#)

April 16, San Jose, CA: During the Embedded Systems Conference, Altera will demonstrate how to create embedded systems that are power-efficient and run off batteries. Learn how to dynamically modify FPGA power utilization using intelligent power management in this 90-minute session.

Support & Literature

[Design Guides: Develop Embedded Systems with Altera FPGAs \(PDF\)](#)

These new design guides explain the best practices for developing embedded systems with Altera. If you're new to Altera's embedded products, the design guides will bring you up to speed quickly. Experienced users will learn how to improve efficiency and reduce design cycles.

Download today!

From the Forum: [Cyclone III Configuration & Voltage](#)

Altera Forum user **robwillcarp** asked about the configuration voltage for Cyclone III FPGAs—2.5V or 3.3V? Read the suggestions from other Forum users that helped answer his question, then post your own advice to gain Forum reputation today!

Altera in the News

[FPGAs Help Drive Automation](#), EE Times

To help automotive designers reduce board complexity by taking on more functionality and system integration, FPGA vendors are tailoring devices to offer low-cost connectivity and DSP capabilities. With Altera's proven solutions, automotive OEMs can effectively create electronics that meet all quality standards.

Most Popular News from Last Issue

[Design Example: Say Goodbye to Your Configuration Controller](#)

Configure your FPGA design without a configuration device. Download the Application Selector Design Example to learn how, using your Nios II Embedded Evaluation Kit. The "Configuring Cyclone III Devices" chapter of the Cyclone III Handbook provides helpful details, as well.

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