

February 2008 *Inside Edge*

Bringing you the latest Altera® product and solution news.

Monthly Spotlight

[Download Seven Winning White Papers](#) (PDF)

Chosen from hundreds of entries to the 2007 "Innovate Nordic Design Contest," these winning white papers show how engineers use Altera's IP, tools, and development kits to build market-specific applications. Featured white papers include:

- [Digital Stereo Technology](#)
- [Software Defined Radio with SSB Demodulation Voiceprint Authentication](#)
- [SOPC-Based Voiceprint](#)

FPGAs, CPLDs & ASICs

[World's Largest FPGA Now Available Online](#)

Stratix® III EP3K10K0 FPGAs are now shipping in production. These Stratix III devices feature support for DDR3 memory interfaces at 1,067 Mbps and the lowest power consumption of any high-density, high-performance programmable logic device. Buy online today!

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Software, Intellectual Property & Development Kits

[Download Service Pack 2 for Quartus II Software v7.2](#)

Download service pack 2 for the Quartus® II software today for additional programming support for Stratix III and Cyclone® III devices.

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Technology & End Markets

[New Video: Interfacing 1,067-Mbps DDR3 Memories to FPGAs](#)

Join Paul Evans as he demonstrates how to interface 1,067 Mbps DDR3 memories to Stratix III FPGAs. Using a Stratix III board with a Nexus DDR3 module and Tektronix TLA, Paul shows how the DQS aligns with the data as it goes from the FPGA to the DDR3 module and back. View now!

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Events & Training



See New Device Availability

[Arria GX](#)
[Cyclone III](#)
[MAX IIZ](#)
[Stratix III](#)

Inside Edge Poll

Which high-definition video format do you prefer?

[Take the Poll. See the results.](#)



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[Ten New Course-By-Course Curricula in Altera Training](#)

Sharpen your competitive edge with Altera training! The updated and redesigned training site offers ten course-by-course curricula, making it easier to choose the right courses for you and your team.

[Visit Altera at Embedded World 2008](#)

Feb 26-28, Nurnberg, Germany: At Embedded World 2008, Altera will demonstrate its latest embedded systems technology, including graphics, motion control, and the Nios[®] II processor. Visit the booth for a chance to win a new Nios Embedded Evaluation Kit with color LCD touchscreen!

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Support & Literature

[Timing in Source-Synchronous Interface Designs](#) (PDF)

Learn new techniques for constraining and analyzing the source-synchronous interfaces used for high-speed data transfer. This detailed app note covers a variety of clocking schemes and shows exactly how to compute the necessary constraints through specific application examples.

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Altera in the News

[Arria GX FPGAs Named to EDN's Hot 100 Products List of 2007](#)

Arria[™] GX FPGAs were named to EDN magazine's annual "Hot 100 Products" list in the digital, memory, and programmable ICs category. Products appearing in this list are chosen by its editors from hundreds of newsworthy items appearing in EDN throughout 2007.

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Most Popular News from Last Issue

[Buy New Arria GX Development Kit Online](#)

Get started on your next PCIe, GbE, or SRIO design with a new Arria GX Development Kit. Only \$995, the kit includes everything you need to design transceiver-based Arria GX FPGAs: a full-featured board, design software, complete documentation, and reference designs.

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