

August 2009

[www.altera.com](http://www.altera.com)

## Monthly Spotlight

### [Order New Arria® II GX Development Kits Today](#)

Cut development time for cost-sensitive applications requiring high-performance 3-Gbps transceivers. With this kit, you can develop and test designs for PCIe, Gigabit Ethernet, and other transceiver protocols. Add one of 20+ available HSMC daughter cards for even more functionality.



### FPGAs, CPLDs & ASICs

#### [Designing a Power Distribution Network \(PDN\) for High-End FPGAs](#)

Learn a scientific approach to designing an effective PDN that minimizes board complexity, decoupling requirements, and system costs.

#### [Multi-Channel PCIe DMA Controller & Video Reference Design](#)

Request OmniTek's controller that handles data transfer over a PCIe Gen1 or Gen2 bus, configurable for x1, x2, x4, and x8 lane PCIe.



### Software, Intellectual Property & Development Kits

#### [Purchase Quartus II Software & Get a Free Nios II Soft Processor](#)

Special offer extended! Purchase Quartus® II Subscription Edition software for \$2,495 and get the industry's # 1 soft processor for free!

#### [Increase Your Team's Productivity With the Right FPGA Software](#)

Learn how using the right FPGA software helps your global team work more efficiently, cuts months off design time, and reduces manufacturing risk.



### Technology & End Markets

#### [White Paper: Enabling Image Format Conversion in FPGAs](#)

Learn how Altera's 1080p video design framework makes it easy for system designers to create a custom image HD format conversion signal chain.

#### [Video: Accelerate Video Format Conversion Using 1080p Framework](#)

See an easy method for up-conversion, down-conversion, and cross-conversion of SD and HD video streams.

#### [Rapid Military Sensor DSP Design With FPGAs](#)

In this webcast, learn how to optimize large designs' performance while reducing design iterations.

### Events & Training

#### [Register for IBC 2009 to See Altera's 1080p & PCIe Solutions](#)

Sept 11-15, Amsterdam: Check out our multi-channel triple-rate SDI-to-PCIe Gen2 bridge solution for fast 1080p development, and more!

#### [Build a Digital Picture Viewer: Nios II Design Tutorial \(PDF\)](#)

Learn how to use SOPC Builder in Quartus II software and Nios® II EDS to build a digital picture viewer for the Nios II Evaluation Kit.

#### [New Class: Best Practices for Maximizing FPGA Design Productivity](#)

Learn how to organize your design for incremental compilation, use testbenches for functional verification, and close timing quickly.

### Did You Know...

Clicking a link with the scrolling wheel of your mouse opens the link in a new window behind your active window.

## Support & Literature

### [Receive Technical Updates on Documentation Changes](#) **Free**

Sign up to be notified when one product's or product category's documents change, then subscribe to updates of specific documents.

### [New Device Schematic Review Worksheets](#)

Quickly find mistakes in schematics and ensure adherence to device pin connection guidelines using these schematic review worksheets.



[Click to enlarge](#)

[From the Forum: Using SODIMM DDR2 Module on Cyclone III FPGA rws28](#) wants to know if SODIMM DDR2 module can be used with a Cyclone® III FPGA. Read the comments and post your advice in the Forum today!

## Altera In the News

[The Energy Aware Home](#), [appliancedesign.com](#)

Home area networks let utility companies remotely monitor consumer energy use and make homeowners more aware of their energy consumption.

## Most Popular News from Last Issue

### [Download PCI Express High-Performance Reference Design](#)

The PCIe high-performance reference design highlights the performance of the hard implementation of Altera's PCIe MegaCore® function. It has two high-performance chaining DMA engines that transfer data between the Stratix® IV GX FPGA's internal memory and the PC's main memory.

---

To ensure that you receive future issues of Inside Edge, please add [announcements@altera.com](mailto:announcements@altera.com) to your address book.



As a subscriber to the Inside Edge, you will receive a monthly email newsletter. To unsubscribe from this newsletter or subscribe to additional Altera email updates and enewsletters, please visit our [Email Subscription Center](#).

Altera email communications include:

- Product Announcements & Updates
- Webcast & Video enewsletter
- Embedded enewsletter
- DSP enewsletter

Copyright © 1995-2009 Altera Corporation, 101 Innovation Drive, San Jose, California 95134, USA

ALTERA, ARRIA, CYCLONE, HARDCOPY, MAX, MEGACORE, NIOS, QUARTUS & STRATIX are Reg. U.S. Pat. & Tm. Off. and Altera marks in and outside the U.S.