

## Monthly Spotlight

### [Download PCI Express High-Performance Reference Design](#) Free

The PCI high-performance reference design highlights the performance of the hard implementation of Altera's PCIe MegaCore<sup>®</sup> function. It has two high-performance chaining DMA engines that transfer data between the Stratix<sup>®</sup> IV GX FPGA's internal memory and the PC's main memory.



## FPGAs, CPLDs & ASICs

### [Now Shipping: New Low-Power FPGAs With Security](#)

With up to 200,000 logic elements using less than 0.25W of static power, Cyclone<sup>®</sup> III LS FPGAs are the lowest power FPGAs with a full suite of security features. These features protect against tampering and reverse engineering, while offering information assurance.

### [White Paper: Protecting FPGA Designs From Common Threats \(PDF\)](#)

As counterfeiting and IP theft increase, so do concerns about design and IP security. Cyclone III LS FPGAs offer security features on a low-power platform, so designers can protect the bitstream and device configuration from cloning or tampering while meeting power requirements.

### [Reduce FPGA Verification Time With GateRocket's RocketDrive](#)

Read how GateRocket's easy-to-use RocketDrive verification system can help accelerate verification cycles and improve time-to-market for high-density Stratix IV FPGA designs. RocketDrive integrates seamlessly with Quartus<sup>®</sup> II design software and requires no changes to source files.

## Software, Intellectual Property & Development Kits

### [Save \\$500 on Video and Image Processing IP Suite](#)

Altera's Video and Image Processing (VIP) Suite includes 15 video IP cores, including scaling and deinterlacing. Save \$500 on the suite when you also purchase Bitec's Cyclone III Video Development Kit, which supports image processing designs using the VIP Suite.

### [Download Quartus II Software v9.0 Service Pack 2](#)

Quartus II software v9.0 service pack 2 includes support for Cyclone III LS FPGAs, enhanced PCI Express support for Stratix IV GX FPGAs, and additional software updates for enhanced device support. Download SP2 today!

### [Download the Latest Power Distribution Network Design Tool](#)

Download the easy-to-use power distribution network (PDN) design tool to optimize board-level PDN and the number of capacitors for each power plane. This latest version of the PDN tool includes the PCB cutoff frequency of Stratix IV devices / packages.

## Technology & End Markets

### [White Paper: Design Separation for Reliability & Info Assurance \(PDF\)](#)

Traditional redundant system designs often lead to increased component count, logic size, system power, and cost. The Cyclone III LS FPGA's design separation feature addresses these conflicting needs while maintaining high reliability and information assurance.



[Click to enlarge](#)

## Events & Training

### [Prepaid Training Credits for Discounted Training](#)

Get high-quality instructor-led training at a discounted price when you purchase a corporate training agreement from Altera. Agreements provide prepaid training credits for regularly scheduled classes or private training at your site. (Offer valid in North America only.)

### [Register for the Asia Pacific Technology Roadshow 2009](#)

Aug 4-Sept 24: Altera's annual one-day, technology-focused seminars are coming to 16 cities around Asia Pacific. View live demos to learn about 40-nm technology and tools, and enhancing DSP and embedded designs. The MathWorks, Linear Technology, Tektronix, and other partners will attend.

## Support & Literature

### [From the Forum: SRAM and VGA Problem?](#)

Altera Forum user **brosnan** is trying to show a 1,024 x 768 x 8 grayscale image on his LCD monitor. Read the comments and suggestions from other Forum users that helped answer his question, then post your own advice to gain Forum reputation today

## Altera in the News

### [Use FPGAs for Energy-Efficient Motor Control](#), Industrial Embedded Systems

Modify motor control systems and increase performance in industrial applications by using FPGAs. FPGAs can be quickly and inexpensively reconfigured for new applications while maintaining a high degree of system interoperability using Ethernet and fieldbus technologies.

## Most Popular News from Last Issue

### [Special Offer on Nios II Embedded Kit & IP Bundles](#)

Save \$500 when you purchase a Nios<sup>®</sup> II Embedded Evaluation Kit or an Altera<sup>®</sup> Embedded Systems Development Kit, Cyclone III Edition, with an Embedded IP Suite. This special offer is not available online, so order through your local distributor or Altera Telesales at 1-888-800-0631.

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