

June 2009

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## Monthly Spotlight

### [Special Offer on Nios II Embedded Kit & IP Bundles](#)

Save \$500 when you purchase a [Nios® II Embedded Evaluation Kit](#) or an [Altera® Embedded Systems Development Kit, Cyclone® III Edition](#), with an Embedded IP Suite. This special offer is not available online, so order through your local distributor or Altera Telesales at 1-888-800-0631.



## FPGAs, CPLDs & ASICs

### [Download New Reference Designs for Arria II GX FPGAs](#)

Download four new reference designs for Arria® II GX FPGAs to jump start your next design: PCI Express-to-DDR2 SDRAM, 10-Gigabit Ethernet, Triple-Rate SDI Loopback, and DUC and DDC Digital IF Modem for WiMAX. Arria II GX FPGAs are now shipping!

### [White Paper: FPGAs With Optimized >10 Gbps Transceivers \(PDF\)](#)

Learn about key technologies that enable Stratix® IV GT FPGAs, with integrated 11.3-Gbps transceivers, to support 40G/100G applications. Key technologies, including LC-based oscillators and decision-feedback equalization, as well as transceiver test results, are shown.



Poll

[Where do you go for technical support?](#)

## Software, Intellectual Property & Development Kits

### [Purchase Quartus II Software and Get Nios II Soft Processor Free](#)

For a limited time, get both Quartus® II Subscription Edition and the Nios® II soft processor--the industry's #1 soft processor--for \$2,495. This package provides a full license for 12 IP functions, including the Nios II processor, NCO, FIR, QDR II, and DDR1/2/3 memory controllers.

## Technology & End Markets

### [White Paper: Panoramic Views by Stitching Fisheye Images \(PDF\)](#)

See how to use FPGAs to generate panoramic views for automotive applications. By stitching multiple fisheye images together, a complete view of a vehicle's surroundings is generated for safer vehicle control and navigation.

### [White Paper: Coprocessing for Sustained Peak Performance \(PDF\)](#)

Explore FPGA algorithm implementations that ensure sustained performance is closer to the device's peak performance than a quad-core CPU's. This performance isn't power-hungry--even the largest FPGA's power consumption is well below datacenter power and cooling limits.

## Events & Training

### [Keep Your Skills Current with Free Online Training Classes](#)

Sharpen your competitive edge and ensure that your design skills are up-to-date with convenient, comprehensive online training courses. For example, learn the latest techniques to create, manage, and compile Quartus II projects, or accelerate Nios II software with the C2H Compiler.

## Support & Literature

### [From the Forum: Using Stratix II FPGA for Wireless Communication](#)

Altera Forum user **sonaiko** is working on a short-range wireless system using coil-coil communication between two FPGAs, but is running into issues. Read the comments and suggestions from other Forum users,



New Webcasts

[Upgrade Your System's Resistive Touch Screen to Multi-Touch](#)

[How to Switch from](#)

then post your own advice to gain Forum reputation today!

### [Tell Us the Good, the Bad, the Ugly About the Quartus II Handbook](#)

If you use the Quartus II Handbook, take a minute to tell us your thoughts on it. We strive to help you increase your productivity, and your feedback will help us improve our documentation. We'd love to hear from you!

### Altera in the News

[Electronics: The Energy Aware Home](#), Appliance Design  
Connecting appliances to a home area network allows homeowners to monitor and control energy use. See how low-cost FPGAs help developers implement greater functionality and processing power into home area networks while reducing the overall cost.

### Most Popular News from Last Issue

#### [White Paper: Using FPGAs to Render Graphics and Drive LCDs \(PDF\)](#)

scalable and graphics can be generated by any external processor, embedded processor, or hardware graphics acceleration engine in the same FPGA.

[Xilinx ISE Software to Quartus II Software](#)

[WiMAX System Design Using Simulink and DSP Builder](#)



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