

Monthly Spotlight

[New Altera Product Selector Tool: Compare Devices on 75 Parameters](#)

This new parametric product selector tool allows you to find and compare specifications and features of our FPGA, CPLD, and ASIC devices using over 75 parameters. Compare specific devices side-by-side by filtering the criteria you choose for analysis.



FPGAs, CPLDs & ASICs

[Arria II GX Family Now Shipping in Production Volumes](#)

Altera's successful 40-nm rollout continues! Two more Arria® II GX devices are now shipping in production volumes, the EP2AGX45 and EP2AGX60.

Software, Intellectual Property & Development Kits

[Order New Stratix® IV E FPGA Dev Kit with 533-MHz DDR3-DIMM](#)

Accelerate your prototyping designs, including 533-MHz DDR3-DIMM designs, and take advantage of the 530K logic elements offered by this kit.



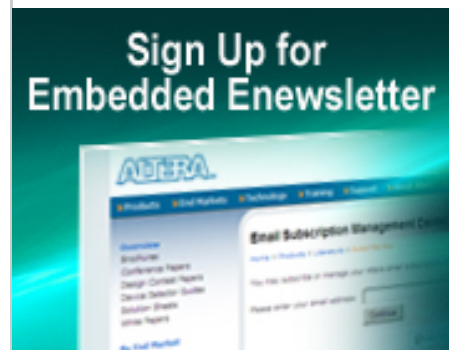
Technology & End Markets

[Ref Design: Rapid Video Format Conversion Reduces Time to Market](#)

Implement multi-channel, high-quality cross conversion of standard- and high-definition video streams in interlaced or progressive format.

[Video: See 40-Gbps OTN Mapper Demo Using a 40-nm FPGA](#)

See how you can build a 40-Gbps multiplexed transponder that meets stringent cost and power requirements and has a wide range of capabilities.



Support & Literature

[App Note: Enhanced Real-Time Performance for the Nios II Processor](#)

The new Nios® II Vector Interrupt Controller (VIC) enhances real-time performance. Download the application note today.

[App Note: Anti-Tamper Protection for Cyclone III LS Devices](#)

Protect your design with Cyclone® III LS security. Learn more about security with the design example and the Cyclone III LS development kit.



[Click to enlarge](#)

[Altera Forum: FPGA Drive Current Effect on Power Draw](#)

Forum user Kathryn is debugging a signal integrity issue on a Cyclone II test board. Post your advice to gain forum reputation points today!

Events & Training

[Free Online Training: What's New in Quartus II Software v9.1](#)

Learn to take advantage of the latest productivity features in Quartus® II software v9.1 and learn about the new Cyclone IV family.

Altera In the News

[Evaluating the FPGA Selection Process in Military System Design](#)

Learn about a structured methodology for evaluating which FPGA solution best fits a design project's requirements.

Most Popular News from Last Issue

[The Nios II/e Core is Now Free](#)

Designed to use the fewest FPGA logic and memory resources of the Nios II processor family, the Nios II/e core is free in Quartus II software v9.1.

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