

## Monthly Spotlight

### [Webcast/White Paper: Enable High-Precision, High-Performance DSP](#)

Learn how the industry's first variable-precision DSP architecture in Stratix® V FPGAs allows you to set the precision of each DSP block—from 9x9 up to floating point—to perfectly suit your application. Watch the [webcast](#) and read the related [white paper](#).



[View the Free Webcast](#)

## FPGAs, CPLDs & ASICs

### [Webcast/White Paper: Extending Transceiver Leadership at 28 nm](#)

View an architectural exploration of SERDES challenges and solutions for 12.5-Gbps backplanes and next-generation 28-Gbps optical modules in Stratix V FPGAs. Watch the [webcast](#) and read the related [white paper](#).



[Learn More](#)

### [In-Stock Availability for Many Cyclone Series FPGAs—Buy Online](#)

Accelerate your engineering schedules—buy Cyclone® FPGAs at our online store to get them in your hands as fast as possible.

## Software and Development Kits

### [New Dev Kit for USB 3.0 Subsystems Using Cyclone III FPGAs](#)

PLDA's SuperUSBC3-55 Kit provides a hardware/software environment for prototyping and deploying SuperSpeed USB Device-based applications.



## Technology and End Markets

### [Keynote Video: Get Insights Into Altera's DSP Strategy](#)

Watch this 5-part video to hear Danny Biran, Altera's senior vice president of marketing, explain system-level trends in today's DSP systems. [Part 1](#), [Part 2](#), [Part 3](#), [Part 4](#), [Part 5](#).

### [June 9 Webcast: Leverage FPGA Flexibility for Drive/Motion Systems](#)

Join Altera and Global Spec on June 9 for a live webcast on the advantages of using FPGAs in drive and motion control systems. Register today!



[Learn More](#)

### [Visit Altera at the Motor Control Virtual Conference Anytime](#)

See how to add intelligence to motor control functions, options for MCU implementation and the cost benefits of FPGAs in Altera's virtual booth.

### [White Paper: EO/IR Systems with Image- and Sensor-Processing IP](#)

Explore how implementing these IP functions on Cyclone IV FPGAs can kick-start development efforts for next-generation EO/IR and display systems.



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## Support and Literature

### [Altera Forum: PCI Express SOPC Flow Hardware Ready Example](#)

Check out this user-created reference design on how to use PCI Express® in an SOPC flow (including Arria® II GX FPGAs). Join the Forum today!

## Training and Events

### [Free, Updated Online Training: Configuring Altera FPGAs](#)

Learn the configuration schemes, solutions, features, and tools that configure Altera FPGAs, including Cyclone IV and Stratix® V FPGAs.

### [Visit Us at the EE Times FPGA Virtual Conference, June 24](#)

Visit our booth to see how you can use FPGAs to achieve higher system integration and meet high-bandwidth needs while lowering cost and power.

## Altera In the News

### [Altera Captures Two EDN Innovation Awards](#)

[Quartus® II Software version 9.1](#) wins EDA: Back-End Tools award.

[1080p Video-Design Framework](#) wins Design Framework award.

### [The Need for Variable-Precision DSP Architecture, Programmable Logic DesignLine](#)

FPGAs with variable-precision DSP block architecture enable high-precision and high-performance signal processing.

## Most Popular News from Last Issue

### [Free Open Source Embedded Linux for the Nios II Processor](#)

Visit the Nios® Wiki to get help for everything from installing Linux on your desktop to creating a Nios II design with a memory management unit.

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