

## Monthly Spotlight

### [White Paper: Technology Needs for 40G–100G Net-centric Operations](#)

Discover how Altera's unprecedented combination of technological advances enables advanced network developers to implement and deploy military capabilities in a secure and flexible manner while dramatically reducing non-recurring engineering (NRE) and critical time to mission.



## FPGAs, CPLDs and ASICs

### [Cyclone IV FPGAs In-Stock for Immediate Shipment—Buy Now](#)

Find many production-qualified, errata-free Cyclone® IV devices at Altera's online store. Order devices today for your prototype designs!



## Software, Intellectual Property, and Development Kits

### [7 Ways to Reduce Your Compilation Time](#)

Cut your compile times by using 7 techniques in this updated Quartus II Handbook chapter for version 10.0.

### [White Paper: Guaranteeing Silicon Performance with Timing Models](#)

Read how Altera® timing models provide a simple way to verify FPGA design timing without performing full electrical extractions and simulations.



### [Demo: Using Quartus® II Transceiver Toolkit to Verify Signal Integrity](#)

Achieve low-cost, low-power designs. New kit has transceivers at speeds up to 6.375 Gbps. Supports DDR3 memory interfaces and popular protocols.

### [How to Debug Your Transceiver Links Using Quartus II Software](#)

This transceiver toolkit chapter shows how to test transceiver channels and improve the signal integrity of transceiver links in your design.



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## Technology and End Markets

### [Video Demo: Accelerate Video Processing Using Reference Designs](#)

Want an integrated solution that makes developing broadcast infrastructure equipment easier and faster? Watch the demo to see how.

### [White Paper: Battery-Free, FPGA-Based RAID Cache Solutions](#)

Explore how SEU mitigation enhancements developed for Stratix® V FPGAs provide a strong roadmap to address soft-error system challenges.

### [Leverage Flexible FPGAs in Machine Vision \(MV\) Systems](#)

Learn how Altera FPGAs provide flexibility and connectivity advantages to MV systems and allow integration with a wide range of functions in one device.

## Training & Events

### [Free, Updated Online Training: Basics of Programmable Logic](#)

Take this training to learn the history of FPGAs and get an overview of what's inside of a device and the tools of today.

**[Free, Updated Online Training: Read Me First!](#)**

This training is a starting place to quickly understand and use Altera products, collateral, and resources.

**[Attend the Asia Pacific Technology Roadshow 2010](#)**

Altera and distributors are hosting technology-focused seminars in 16 cities in China, Korea, Singapore, Malaysia, Taiwan, and India, from Sept - Nov 2010.

**Altera In the News**

**[Building an IP Surveillance Camera System with a Low-Cost FPGA, Industrial Embedded Design](#)**

Lower product cost and power consumption push FPGAs down the technology curve, while taking advantage of leading-edge process technology.

**Most Popular News from Last Issue**

**[Introducing Virtual Classrooms—Instructor-Led Classes Now Online](#)**

Get the benefits of Altera's traditional instructor-led training from the comfort of your home or office. New virtual classrooms offer the same content as on-location training. Interact with a live Altera expert instructor and get answers to your tough questions. [See the schedule.](#)

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