



[Building High-Speed Memory FPGA Interfaces](#), 15 minutes

View this 15-minute webcast to explore new 40-nm FPGA architectures built for reliable high-speed memory interface operation. You'll learn how to use IP to quickly and easily build memory interfaces, and how soft IP buys back timing margin by reducing timing variations.

[View Now](#)



[Develop Software for Embedded Systems on FPGAs](#), 5 minutes

Are you new to embedded systems on FPGAs, or considering adding a processor to your next FPGA design? In this new video, learn how to build embedded software applications quickly and easily, and how to improve embedded systems' performance with hardware accelerators.

[View Now](#)



[Scale Packet Transport Networks to 40 Gbps Using FPGAs](#),

25 minutes

Learn about flexible standard products which target packet transport applications, built with Altera® FPGAs and TPACK's Carrier Ethernet technology. You'll see how new 40-nm FPGAs enable Carrier Ethernet solutions at 40 Gbps, and about a new universal line card.

[View Now](#)



[Manage Performance & Power Using 40-nm FPGAs](#), 30 minutes

Get a behind-the-scenes look at new technologies such as strained silicon and triple-gate oxide, new transceiver technology for the highest system bandwidth, and power optimization via the FPGA architecture and design software.

[View Now](#)



[Understand & Control Cost Drivers of Military FPGA Designs](#),

20 minutes

Designers of military electronics are faced with unique technical and cost design risks. This webcast looks at ways to mitigate these risks using high-end software tools, the impact of software productivity on defense program costs, and how an efficient software flow can save money.

[View Now](#)

Altera respects your privacy. If you no longer wish to receive this enewsletter, simply [unsubscribe](#).

[Subscribe](#) to additional Altera email updates and enewsletters, or view/edit all of your Altera email subscriptions. Altera email communications include:

- Product Announcements & Updates
- Code:DSP eNewsletter
- Embedded eNewsletter
- Inside Edge eNewsletter

Copyright © 1995-2008 Altera Corporation, 101 Innovation Drive, San Jose, California 95134, USA
Altera, Stratix, Arria, Cyclone, MAX, HardCopy, Nios, Quartus and MegaCore are trademarks of Altera Corporation