



[See 40-Gbps OTN Mapper Demo Using High-Density 40-nm FPGA](#), 6 minutes

Watch this video to learn about the capabilities of our 40-nm Stratix® IV GX FPGA and see our partner TPACK's TPO314 optical transport network (OTN) mapping product in action. Also, see how to build a 40-Gbps multiplexed transponder.

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[Reduce Total System Costs with Market's Lowest Cost, Lowest Power FPGAs](#), 20 minutes

Watch this webcast to learn how you can meet the requirements of your cost-sensitive, bandwidth-intensive applications with Cyclone® IV FPGAs. See how these FPGAs can help you reduce system costs, add new design functionality, lower power consumption, and reduce design time.

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[See 100G Interlaken Solution on 40-nm High-Density FPGA](#), 9 minutes

Learn how to use our 10G Transceiver Signal Integrity Kit, Stratix® IV GT Edition to evaluate signal integrity, and generate and monitor PRBS patterns. In this video, you'll also learn what the industry's lowest power, highest density, high-performance 40-nm FPGAs can bring to your 100G wireline applications.

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[Learn How Dev Kit Utilities Shorten the Design Process](#), 10 minutes

View this video to learn about two easy-to-use, function-rich dev kit utilities, the Board Test System and the Board Update Portal. You'll also find out how the Cyclone III LS FPGA, the largest density low-power FPGA currently shipping, can support your cost-sensitive, bandwidth-intensive applications.

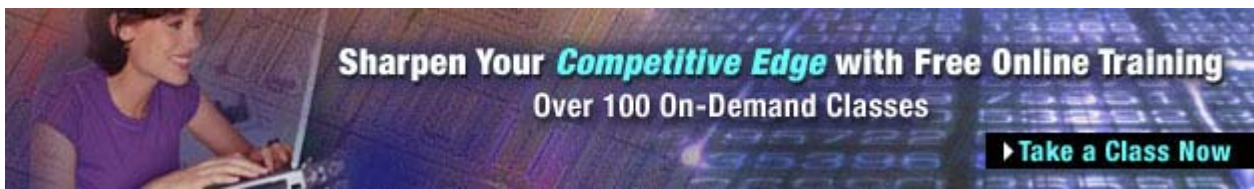
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[5 Easy Steps to Building an Embedded Processor System Inside an FPGA](#), 8 minutes

In this video, you'll learn, step by step, how to build microcontroller functionality into an FPGA in just minutes and see the compact, low-cost BeMicro FPGA-based MCU Evaluation Board in action. Also, see how easy it is to design with the configurable 32-bit Nios® II embedded processor.

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