



[Webcast: Conquer FPGA Design Complexity with System-Level Integration](#), 15 minutes

Watch this webcast to see how you can increase your FPGA design productivity. Find out how to quickly develop large, complex systems by integrating other smaller complete systems and intellectual property (IP).

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[Webcast: Enabling High-Performance DSP with Arria V or Cyclone V Variable-Precision DSP Block](#), 30 minutes

Watch this webcast to learn how the variable-precision digital signal processing (DSP) blocks in our 28-nm Arria® V and Cyclone® V FPGAs enable complex DSP. With five key enhancements in the DSP base blocks, you get higher performance and resource savings.

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[Webcast: Easily Create PCIe-Based Designs for FPGAs](#), 11 minutes

This webcast will show you how to quickly and easily implement a PCI Express® (PCIe®)-based design. Find out how to avoid manually wiring up all the components and focus instead on creating custom logic that differentiates your design. Learn about tools that can help you avoid the complexities of PCIe implementation meet performance requirements, and increase your overall design productivity.

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[Webcast: 5 Reasons to Switch from SOPC Builder to Qsys](#), 20 minutes

It's time to upgrade to a system integration tool! Switch from SOPC Builder to Qsys for more performance and easier and faster design reuse and verification. This webcast shows you five key reasons you need to switch to Qsys today, why switching to Qsys will make your design process more seamless, and how to move your designs to Qsys.

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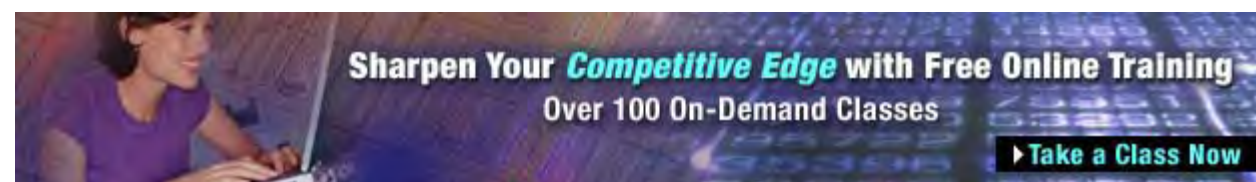


[Video: 5 Reasons to Use a Soft-Core MIPS Processor in Your Next Custom Design](#),

10 minutes

Watch this video to learn five reasons why you should use a soft-core MIPS processor in your next custom design. Get insights about the MP32 processor, the first 100 percent MIPS-compatible soft processor available for Altera® FPGAs and HardCopy® ASICs.

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