



## [Tuning High-Speed Serial Links for Optimal Receive and Transmit Equalization](#), 20 minutes

View this webcast to learn how advanced FPGA transceivers address the challenges of lossy backplanes. You'll also learn how silicon features and simulation tools help you find the optimal receive and transmit equalization settings for a high-speed serial link.

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## [Automate System Generation and IP Integration Using SOPC Builder](#), 20 minutes

Learn how to improve your productivity and cut weeks off your FPGA development time with the SOPC Builder tool. This webcast introduces SOPC Builder and explains how it increases your productivity by enabling you to focus your efforts on custom logic instead of integration tasks.

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## [Interfacing High-Speed ADCs to FPGAs with Embedded Transceivers](#), 5 minutes

Are you interested in the new generation of high-speed ADCs with serial interfaces? Do you believe that gigabit serial interfaces are complex? This new video shows you how easy it is to interface Linear Technology's high-speed ADCs to Altera's embedded transceiver FPGAs.

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## [Scale Packet Transport Networks to 40 Gbps Using FPGAs](#), 30 minutes

Learn about flexible standard products which target packet transport applications, built with Altera® FPGAs and TPACK's Carrier Ethernet technology. You'll see how new 40-nm FPGAs enable Carrier Ethernet solutions at 40 Gbps, and about a new universal line card.

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## [Build 96-Port SGMII GigE with Stratix III FPGAs](#), 6 minutes

Stratix® III FPGAs support SGMII GigE operation on LVDS I/O pins at 1.25 Gbps. With Stratix III FPGAs, you can build communications systems requiring single or multiple (up to 96) Ethernet links quickly and simply while meeting jitter and tolerance requirements. Watch it work in this 6-minute video!

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