



**Upgrade Your System's Resistive Touch Screen to Multi-Touch**, 18 minutes

Adding multi-touch to a wide variety of products may be easier than you think. View this webcast to learn a very cost effective way to add multi-touch to your system using Altera's MAX<sup>®</sup> II CPLDs and surprisingly simple IP.

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**Analyze FPGA Transceiver Interoperability and Signal Integrity**, 8 minutes

View this video to learn how Altera's new Transceiver Signal Integrity Development Kit, Stratix<sup>®</sup> IV GX Edition, enables a thorough evaluation of the transceiver's interoperability and SERDES signal integrity.

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**WiMAX System Design Using Simulink and Altera's DSP Builder**, 60 minutes

In this webcast, you'll see how you can take advantage of FPGAs' high DSP performance with DSP Builder. You'll learn to build highly optimized DSP designs within The MathWorks Simulink, as well as watching a WiMAX wireless system design example.

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**Tuning High-Speed Serial Links for Optimal Receive and Transmit Equalization**, 20 minutes

View this webcast to learn how advanced FPGA transceivers address the challenges of lossy backplanes. You'll also learn how silicon features and simulation tools help you find the optimal receive and transmit equalization settings for a high-speed serial link.

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**Innovating with a Full Spectrum of 40-nm FPGAs and ASICs with Transceivers**, 12 minutes

View this video white paper to see how a full spectrum of FPGAs and ASICs with high-speed transceivers can address your design's performance requirements. You'll learn about high-speed technology trends, and how Altera's FPGA and ASIC portfolio enables system architects to meet their design challenges.

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