

## Designing a Power Distribution Network for High-End FPGAs



## [Designing a Power Distribution Network for High-End FPGAs](#), 25 minutes

View this webcast to learn a scientific way to design an effective power distribution network (PDN) on high-end FPGAs while minimizing board complexity, decoupling requirements, and system costs.

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## [Rapid Military Sensor DSP Design with FPGAs](#), 23 minutes

View this webcast to see how Altera's DSP Builder Advanced Blockset lets you optimize your large design's performance while reducing design iterations. You will also learn how to support multi-channel, poly-phase, high-performance DSP datapaths and efficiently implement FIR and FFT functions.

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## [Partitioning FPGA Designs for Redundancy and Information Security](#), 20 minutes

Ensure high up-time and reliability for designs requiring fault tolerance, redundancy, or information security. View this webcast to see how the new low power, secure Cyclone® III LS FPGAs provide design separation for complete functional independence on a single chip.

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## [Analyze FPGA Transceiver Interoperability and Signal Integrity](#), 8 minutes

View this video to learn how Altera's new Transceiver Signal Integrity Development Kit, Stratix® IV GX Edition, enables a thorough evaluation of the transceiver's interoperability and SERDES signal integrity.


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## [Accelerate Your Video Format Conversion Using 1080p Framework](#), 8 minutes

View this video to learn about an easy method for up-conversion, down-conversion, and cross-conversion of SD and HD video streams. You will also learn how a 1080p format conversion reference design provides a flexible, customizable template for fast conversion, right out of the box.

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