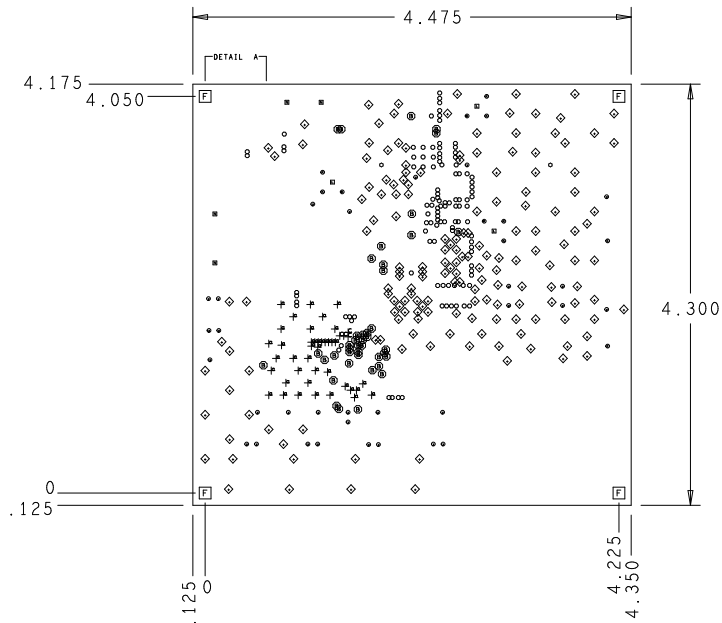


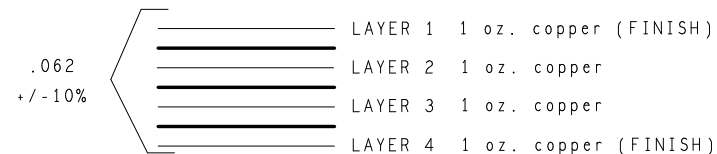
DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
○	8.0	+3.0/-3.0	PLATED	109
●	10.0	+3.0/-3.0	PLATED	42
+	14.0	+3.0/-3.0	PLATED	4
+	15.0	+3.0/-3.0	PLATED	40
◇	20.0	+3.0/-3.0	PLATED	169
•	37.008	+3.0/-3.0	PLATED	9
•	41.339	+3.0/-3.0	PLATED	30
■	92.913	+3.0/-3.0	PLATED	3
■	100.0	+3.0/-3.0	PLATED	4
•	155.12	+3.0/-3.0	PLATED	2
○	200.0	+3.0/-3.0	PLATED	2
□	128.0	+2.0/-2.0	NON-PLATED	4

- NOTES: UNLESS OTHERWISE SPECIFIED.
- BOARD IS VIEWED FROM LAYER 1 (PRIMARY SIDE). TOTAL CONDUCTOR LAYERS ARE 4.
  - THIS DRAWING ALSO EXISTS ON ELECTRONIC MEDIA.
  - MANUFACTURE/FABRICATE TO MEET EU RoHS DIRECTIVE.  
ALL MATERIALS AND SOLDERMASK TO BE COMPLIANT TO EU RoHS DIRECTIVE 20002/95/EC  
LAMINATE AND RESIN MATERIAL PARAMETERS  
Tg > 170 C  
Td > 294 C
  - FINISH:  
ELECTROLISS NICKEL/GOLD(ENIG)PLATING IS TO BE USED. 3-8 uin GOLD OVER 100uin NICKEL.
  - SOLDERMASK SHALL BE LPI(GREEN)OVER BARE COPPER 0.0005"-0.002" THICK,AND CONFORM TO THE LATEST REVISION OF IPC-SM-840.
  - CONDUCTOR WIDTH AND SPACING:  
THE NOMINAL CONDUCTOR WIDTH AND SPACING ON FINISHED BOARD IS:  
WIDTH 0.008"  
SPACING 0.005"
  - SILKSCREEN SHALL BE WHITE NONCONDUCTIVE EPOXY INK, AND SHALL NOT APPEAR ON ANY PADS OR MOUNTING HOLES.
  - PRIOR TO BOARD FABRICATION, COMPARE GERBER DATA TO SUPPLIED IPC-D-356A NET LIST. REPORT ALL DISCREPANCIES.



DETAIL A

LAYER COUPON SHALL CONSIST OF 4.50 x 100 mil SHAPES ON EACH LAYER



LAYER STACKING

SCALE: NONE

Drawn By:	Date Drawn:	EVB-EC7100VQ1 Fabrication Drawing		
Released By:	Date Released:			
Updated By:	Date Updated:			
ALTERA	FILENAME:	MASK#	DWG NO.	REV.
	EVB-EC7100VQ1	N/A	09350	A
SHEET			1 of 1	