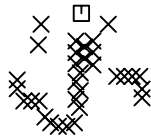


ECN#	ISS.	REV.	DESCRIPTION	DATE	INT.	PG#
-	1	A	INITIAL RELEASE	5-9-13	S	XXXX
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NOTES

MATERIAL: FR4 1 OZ COPPER EACH LAYER.
 ALL HOLES PLATED THRU WITH 1 OZ COPPER MIN.
 FINAL COPPER ON EXTERNAL LAYERS = 2 OZ. MINIMUM.

For best thermal performance,
 Make GND copper as large as
 possible, except on layer 1
 directly under U1.



All spacings have been set
 according to common PCB fabrication
 and assembly practices. If necessary,
 these spacings can be modified according to
 customer manufacturing capabilities.

EN5329-5319 Critical Component Circuit

This layout only shows the critical components
 and traces for minimum footprint with ENABLE as
 a separate signal. Alternate ENABLE configurations,
 and the POK pin need to be connected and routed
 according to the customer application.

Solder mask and paste layer openings should be
 reviewed and optimized to meet customer
 PCB fabrication and assembly guidelines.

Gerber Format: RS274X, 3.5
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SIZE	QTY	SYM	PLATED	TOL
8	32	×	YES	STD
16	1	□	YES	STD

		ENPIRION			
DRAWN BY	DATE	08219, EN5329-5319 GERBER			
S	3-4-13	FAB			
UNLESS OTHERWISE SPECIFIED TOLERANCES: .XX +/- .020 .XXX +/- .010 ANG. +/- 1 DEG.		SIZE	ISSUE	REV.	DRAWING NO.
		A	1	A	.
		SCALE: 2			SHEET: 1 OF 1