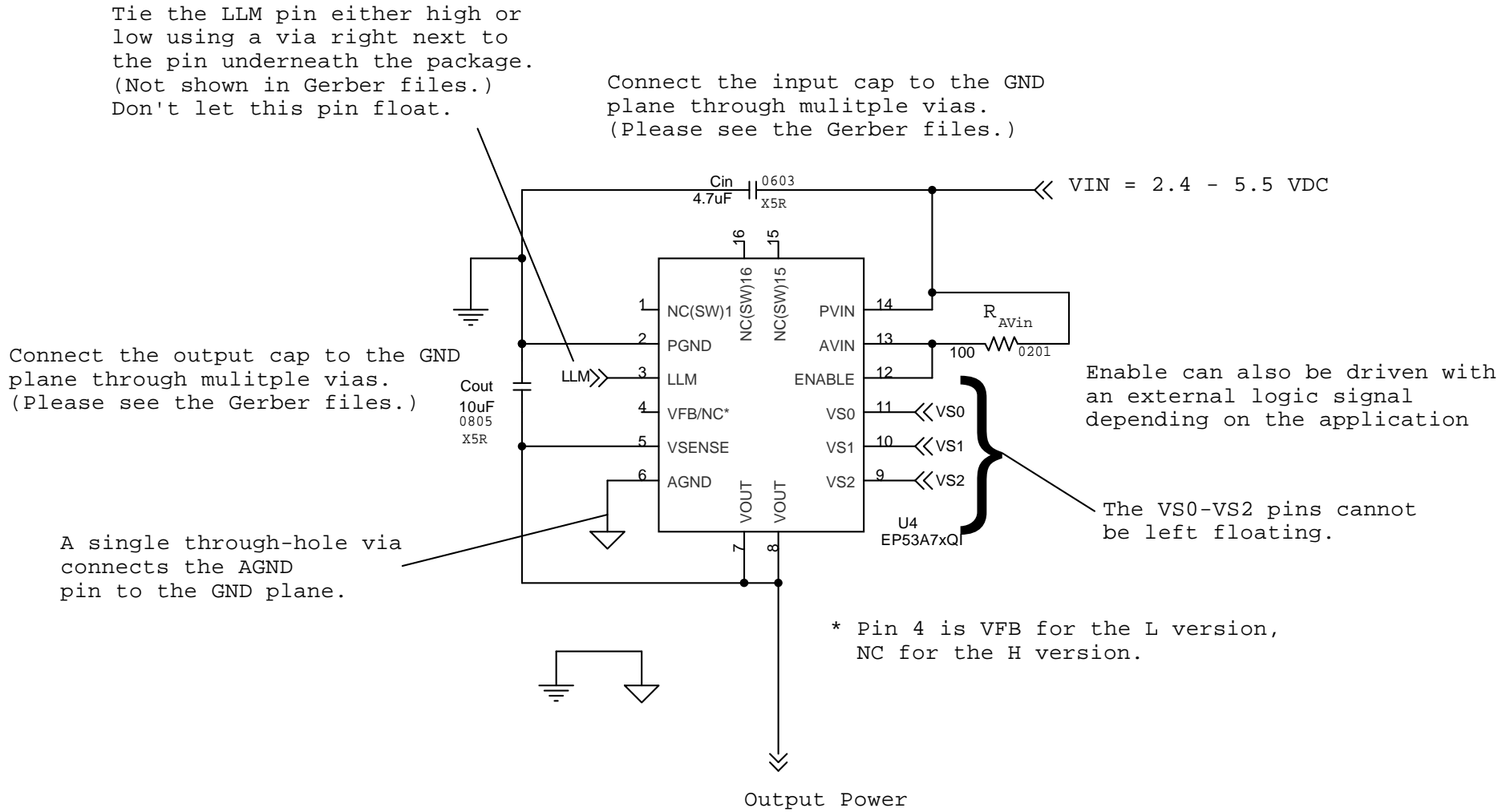



Note: This schematic only shows the critical components and traces for minimum footprint in VID mode with ENABLE tied to AVin. Alternate ENABLE configurations, & other small signal pins need to be connected and routed according to the customer application.



Voltage Programming Connections						
Voltage Selector			EP53A7LQI		EP53A7HQI	
VS2	VS1	VS0	VOUT		VOUT	
0	0	0	1.50V		3.3V	
0	0	1	1.45V		3.0V	
0	1	0	1.20V		2.9V	
0	1	1	1.15V		2.6V	
1	0	0	1.10V		2.5V	
1	0	1	1.05V		2.2V	
1	1	0	0.80V		2.1V	
1	1	1	External Divider*		1.8V	

0 = Connect to the GND plane.
1 = Leave open. Pulled high inside the device.
* Not shown. Refer to the datasheet.

Schematic 03217
Gerbers 03218

			CONFIDENTIAL		
Title EP53AxxQI Critical Components Circuit for Minimum Footprint					
Size	Drawing Number			Drawn By C. Romano	Rev
Date	February 20, 2009			Sheet	of