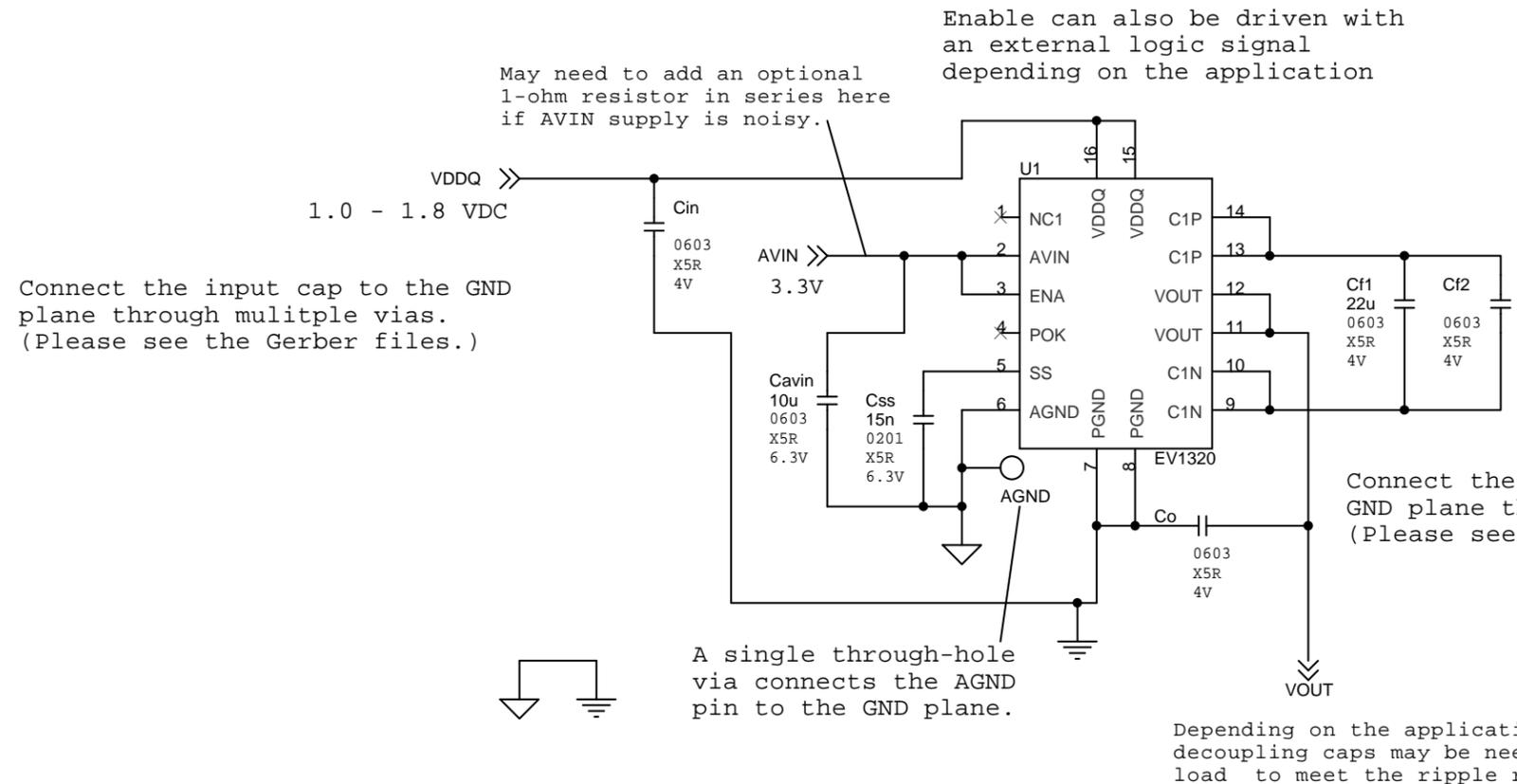


2	Revision	1	Date	Approved

Note: This schematic only shows the critical components and traces for minimum footprint with ENABLE tied to VDDQ. Other ENABLE configurations and the POK pin need to be connected and routed according to the customer application.



Max Iout	Cin	Co	Cf1	Cf2
2 A	22uF	22uF	22uF	10uF
1 A	10uF	10uF	22uF	NOT USED

Schematic 06893  
Gerbers 06894

ENPIRION		<b>CONFIDENTIAL</b>	
Title EV1320 Critical Components Circuit			
Size	Drawing Number	Drawn By AG	Rev
Date	3/16/2012		Sheet of
2			1